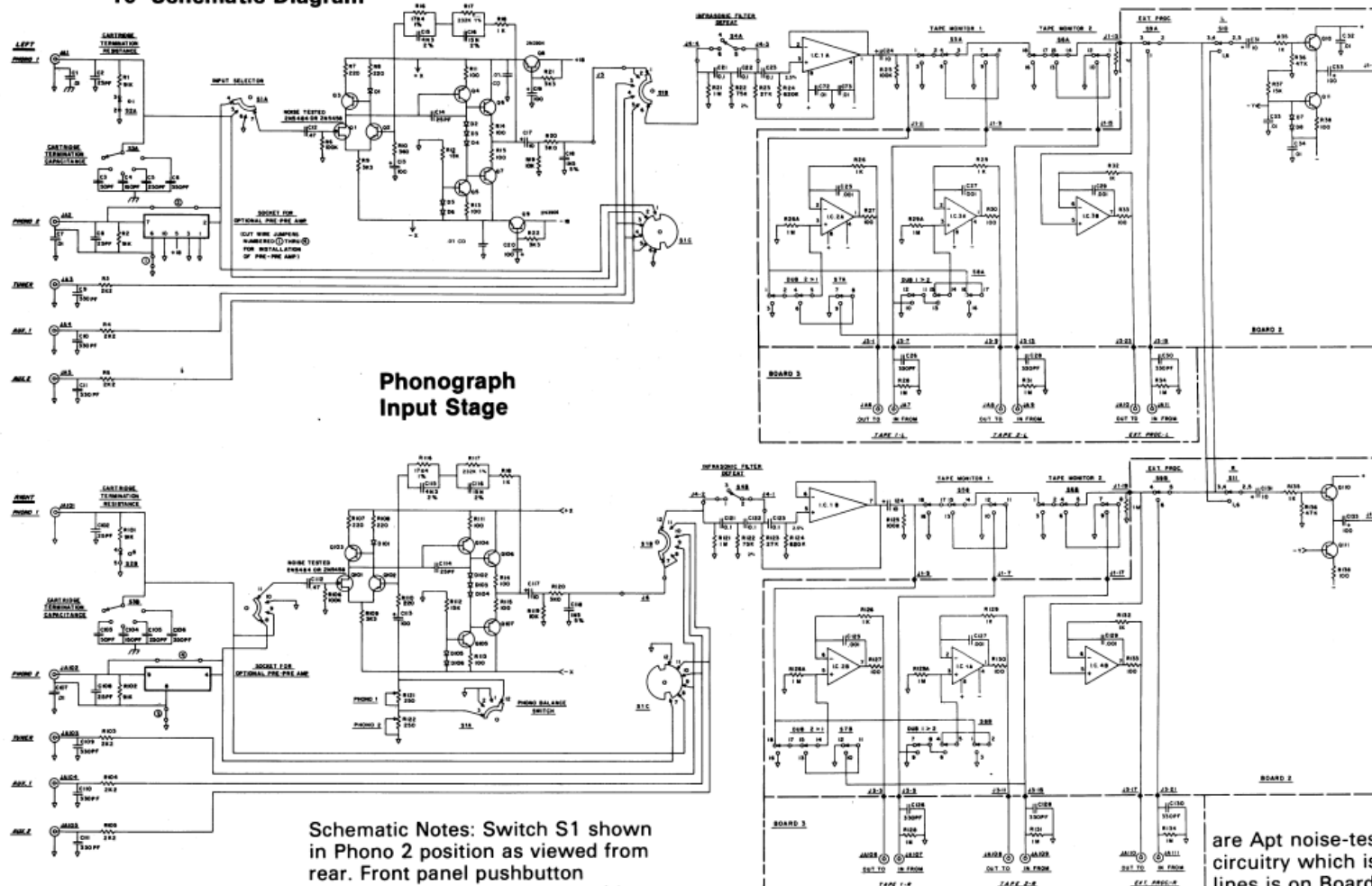


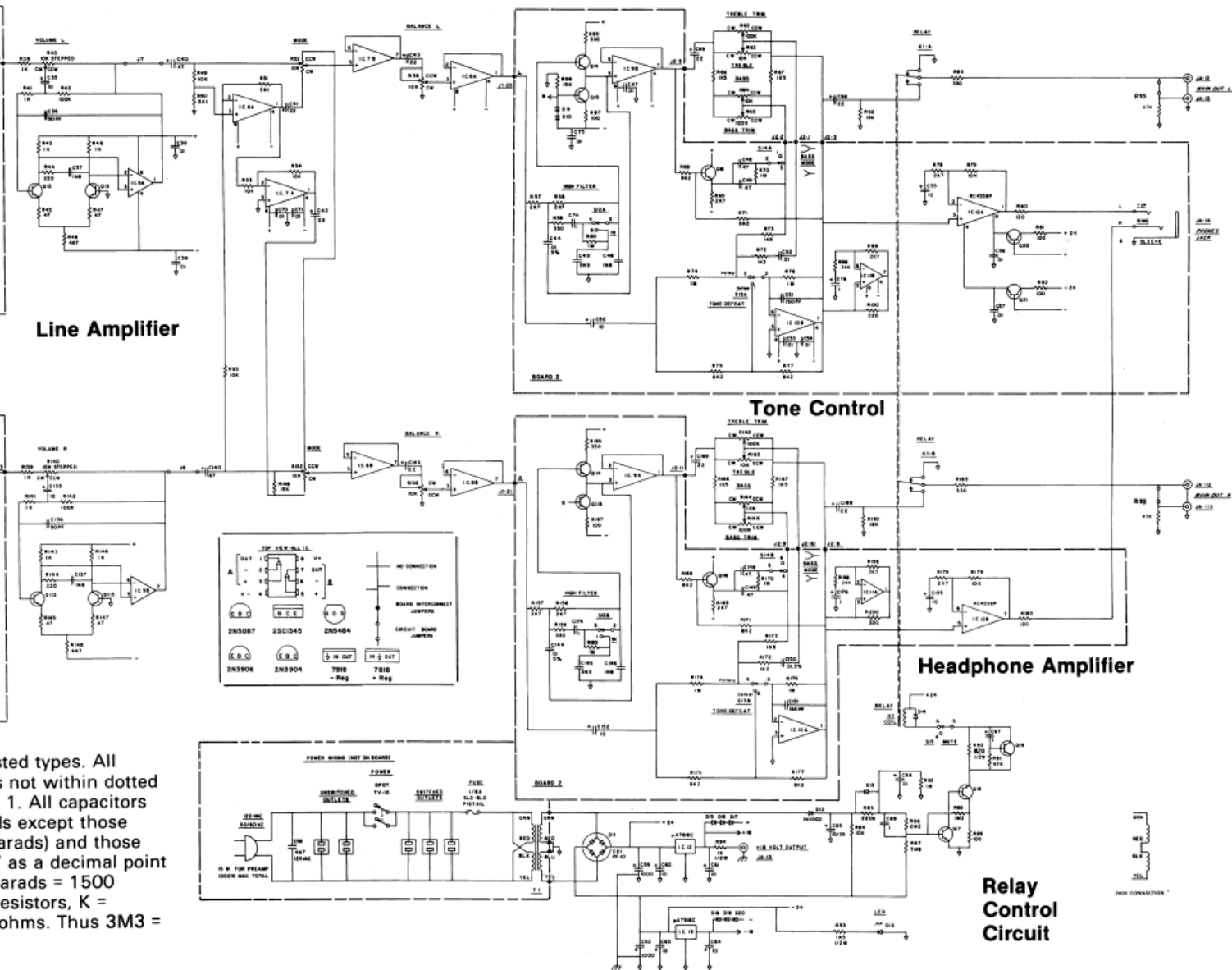
## 10 Schematic Diagram



transistors are 2SC1345E or equivalent except as noted; all *pn*p transistors are 2N5087; all integrated circuits are TL072CP except as noted. Pin 1 and pin 7 of all integrated circuits are at 0 Vdc +1 Vdc. Phono preamp input FETs

are Apt noise-tester circuitry which is on Board 1. All lines are on Board 1. All components are in microfarads (μF) or picofarads (pF) except as noted. (1N5 = 1.5 nanofarads). For resistors, M = megohms, K = kilohms, R = ohms.

## Line Amplifier



tested types. All  
not within dotted  
1. All capacitors  
except those  
arads) and those  
as a decimal point  
arads = 1500  
resistors, K =  
ohms. Thus 3M3 =

## Relay Control Circuit

## 11 Circuit Board Overlays

