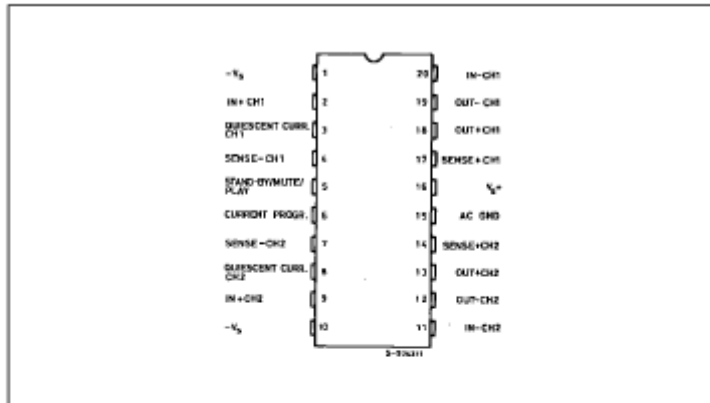


TDA7250

PIN CONNECTION (top view)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_s	Supply Voltage	100	V
P_{tot}	Power Dissipation at $T_{amb} = 60^\circ\text{C}$	1.4	W
T_j, T_{eq}	Storage and Junction Temperature	- 40 to + 150	$^\circ\text{C}$

THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	Max. 65	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, $V_S = \pm 35\text{ V}$, play mode, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_S	Supply Voltage		± 10		± 45	V
I_d	Quiescent Drain Current	Stand-by Mode		8		mA
		Play Mode		10	14	
I_b	Input Bias Current			0.2	1	μA
V_{os}	Input Offset Voltage			1	± 10	mV
I_{os}	Input Offset Current			100	200	nA
G_v	Open Loop Voltage Gain	$f = 100\text{ Hz}$		50		dB
		$f = 10\text{ kHz}$		60		
e_n	Input Noise Voltage	$R_G = 600\text{ }\Omega$ $B = 20\text{ Hz to }20\text{ kHz}$		3		μV
SR	Slew Rate			10		V/ μs
d	Total Harmonic Distortion	$G_v = 26\text{ dB}$, $P_o = 40\text{ W}$ $f = 1\text{ kHz}$		0.004		%
		$f = 20\text{ kHz}$		0.03		%
V_{osw}	Output Voltage Swing			60		V _{pk}
P_o	Output Power (*)	$V_S = \pm 35\text{ V}$, $R_L = 8\text{ }\Omega$ $V_S = \pm 30\text{ V}$, $R_L = 8\text{ }\Omega$ $V_S = \pm 35\text{ V}$, $R_L = 4\text{ }\Omega$		60 40 100		W
I_o	Output Current			± 5		mA
SVR	Supply Voltage Rejection	$f = 100\text{ Hz}$		75		dB
C_M	Channel Separation	$f = 1\text{ kHz}$		75		dB

MUTE / STANDBY/ PLAY FUNCTIONS

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_i	Input Current (pin 5)			0.1		μA
V_{th}	Comparator Standby / Mute Threshold (**)		1.0	1.25	1.5	V
H	Hysteresis Standby / Mute			200		mV
V_{th}	Comparator Mute / Play Threshold (**)		2.4	3.0	3.6	V
H	Hysteresis Mute / Play			300		mV
	Mute Attenuation	$f = 1\text{ kHz}$		60		dB
V_i	Input Voltage Max. (pin 5)		12 (**)			V

(*) Application circuit of Fig. 1 $f = 1\text{ kHz}$; $d = 0.1\%$; $G_v = 26\text{ dB}$.

(**) Referred to $-V_S$.

CURRENT SURVEY CIRCUITRY

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
	Comparator Reference	to $+V_S$ to $-V_S$	0.8 0.8	1 1	1.4 1.4	V
t_d	Delay Time		10			μs

QUIESCENT CURRENT CONTROL

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
	Capacitor Current	Charge Discharge	30 250	60 500		μA
	Comparator Reference	to $+V_S$ to $-V_S$	10 10	20 10	25	mV mV

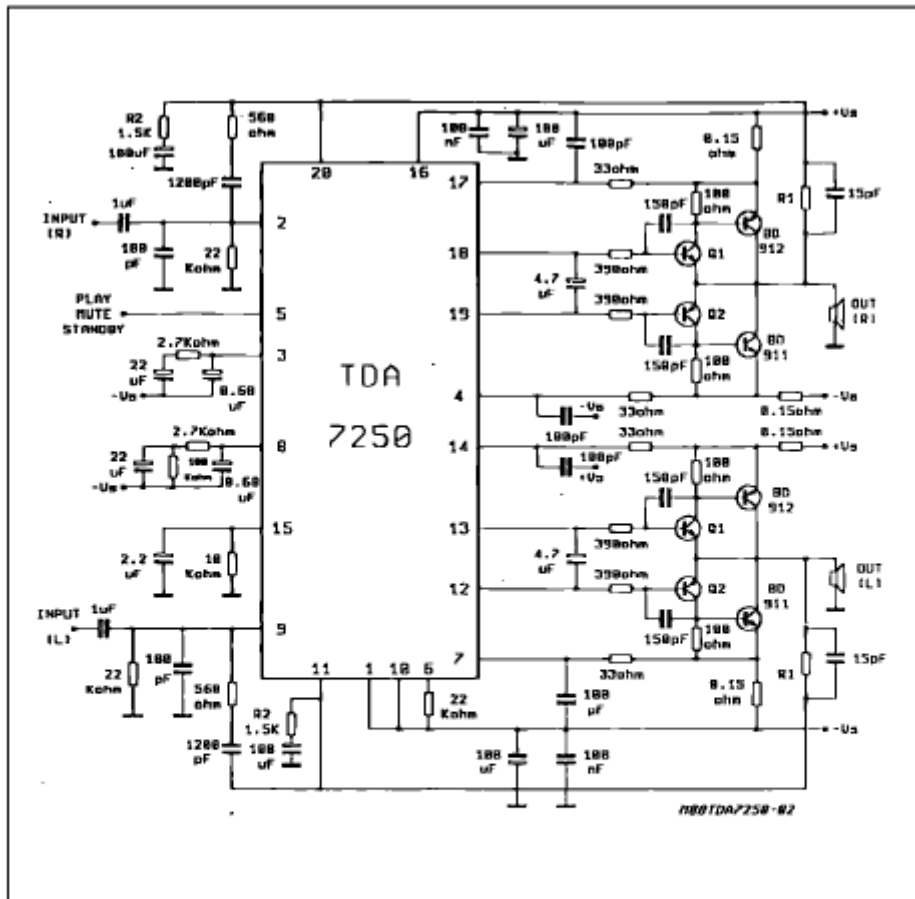


Figure 12 : Suggested Transistor Types for Various Loads and Powers.

$R_L = 8 \Omega$

15W	+30W	+50W	+70W
BDX 53/54A	BDX 53/54B	BDW 93/94B	TIP 142/147

$T_L = 4 \Omega$

30W	+50W	+90W	+130W
BDW 93/94A	BDW 93/94B	BDV 64/65B	MJ 11013/11014

But my application uses 2SC3858/2SA1494 instead of BD911/912 for output . I also used BD681/682 for Q1/Q2.

You can obtain all spesification for TDA7250 on www.st.com .