

## **INCREASING OUTPUT SWING**

Although not required for normal operation, the output voltage swing of the AD8561 can be increased by connecting a 5 k $\Omega$  resistor from the output of the device to the V+ power supply. This configuration can be useful in low voltage power supply applications where maximizing output voltage swing is important. Adding a 5 k $\Omega$  pull-up resistor to the device's output will not adversely affect the specifications of the AD8561.

## **OUTPUT LOADING CONSIDERATIONS**

The AD8561 output can deliver up to 40 mA of output current without any significant increase in propagation delay. The output of the device should not be connected to more than twenty (20) TTL input logic gates, or drive a load resistance less than 100  $\Omega$ .

To ensure the best performance from the AD8561 it is important to minimize capacitive loading of the output of the device. Capacitive loads greater than 50 pF will cause ringing on the output waveform and will reduce the operating bandwidth of the comparator.