

Drill Legend:

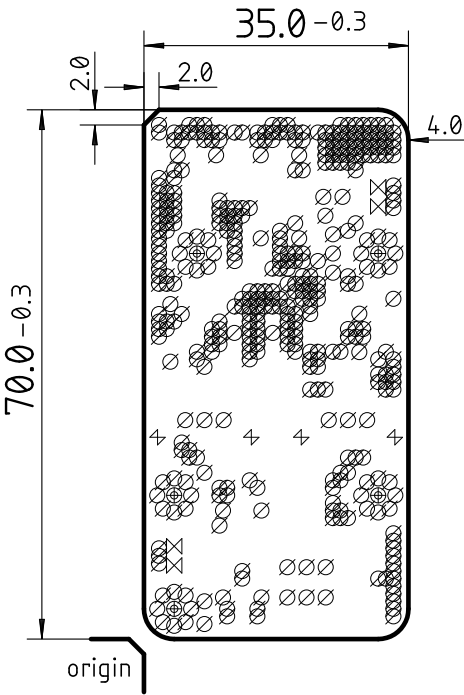
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	366	YES
×	2	32	0.81	10	YES
▣	3	36	0.91	2	YES
◇	4	39	1.00	4	YES
⊠	5	40	1.02	21	YES
⌘	6	47	1.20	4	YES
⦿	7	126	3.20	5	YES

Layer Stack:

SIG-TOP	<div></div>	35µm Cu
SIG-BOT	<div>core FR4</div>	35µm Cu

Layer thickness = 1.6 +/-0.2 mm

View on top side



REV	IND	DATE	CHANGE HISTORY	ISSUE	DIY Community	THIS DRAWING CONTAINS PROPRIETARY INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION	2017 ©	
				DRAWN	TITLE Printed Circuit Board PASS ACP+ M2x			REV
				JPS64 23/1/20				001
				CHECKED	DATE 25.01.2020 15:26			DRG N°
				DATE	FILE: ACP+M2X_00a			PAGE:
001		23.01.2020	Prototyping					