

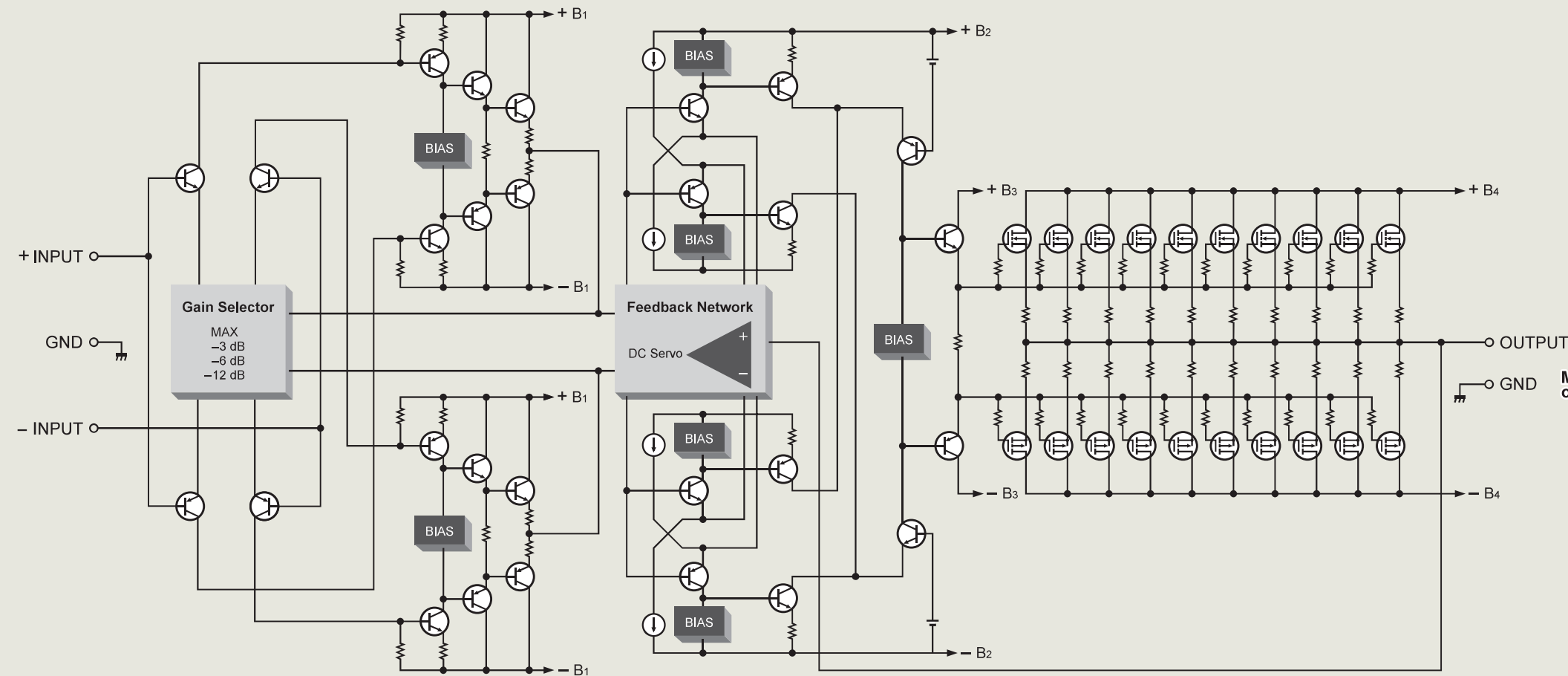
Signal input stage GAIN 22 dB

GAIN < MAX, -3, -6, -12 dB >

Power amplification stage GAIN 6 dB

Voltage amplification stage
MCS+ (Multiple Circuit Summing)

Power amplification stage
Power MOS-FETs in 10-parallel push-pull configuration



■ Circuit diagram of amplifier section

Power MOS-FETs