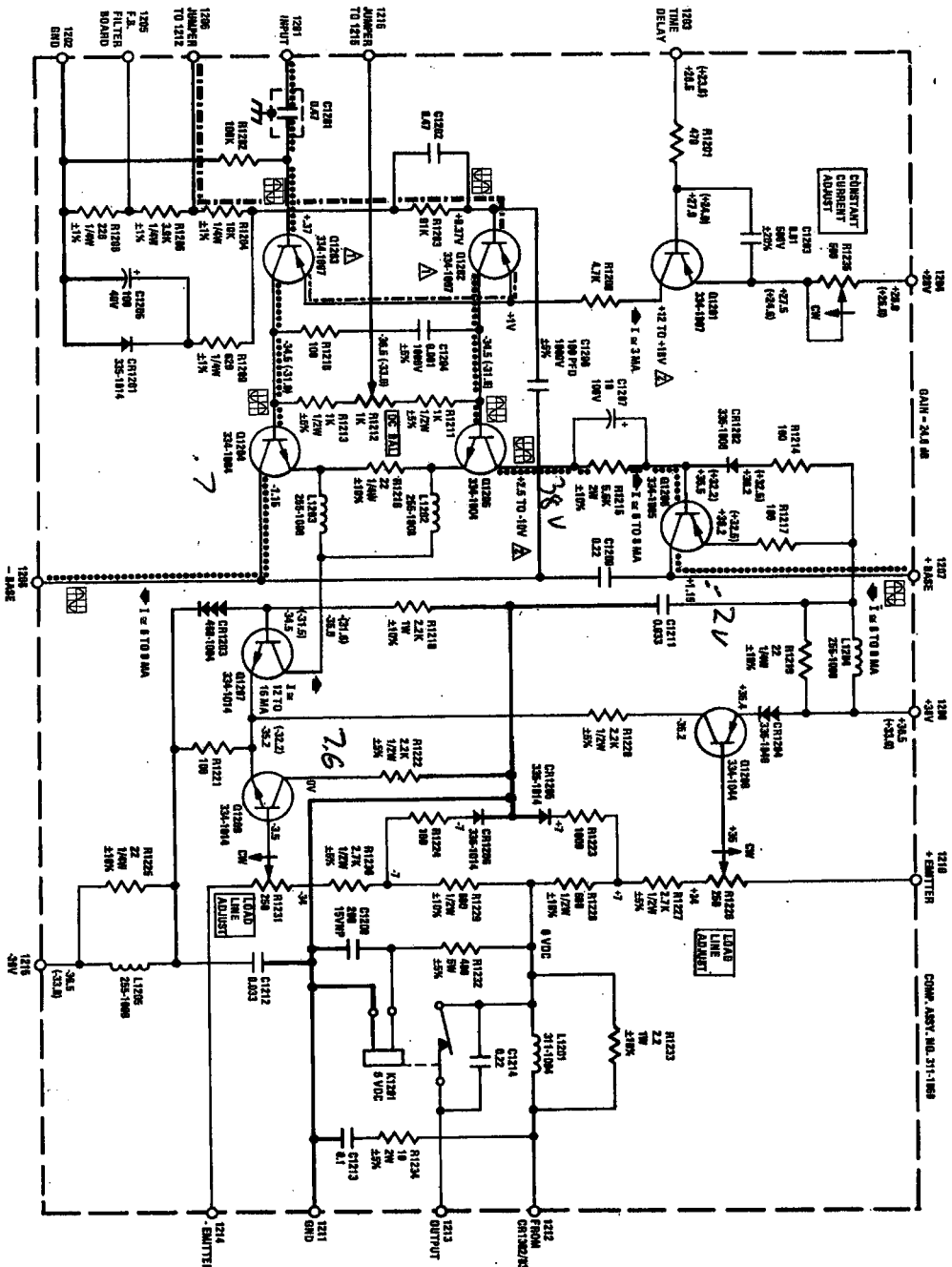


FIGURE 17A - DRIVER SCHEMATIC DIAGRAM

ALL VOLTAGES SHOWN ARE ±10% (UNLESS OTHERWISE NOTED) TAKEN WITH A DC VTM HAVING AN INPUT IMPEDANCE OF 200 MEG-OHMS AND AN AC VTM HAVING AN INPUT IMPEDANCE OF 1 MEGOHM. LINE VOLTAGE EQUALS 120 VAC AT 60 HZ.

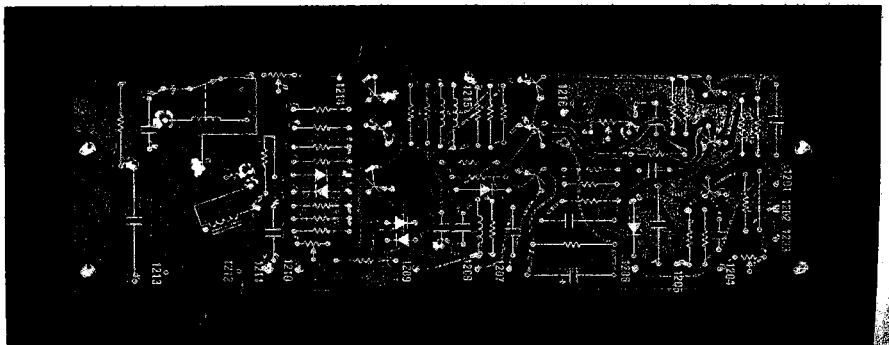
UNLESS OTHERWISE NOTED: ALL RESISTORS ARE IN OHMS %W %K ALL CAPACITORS IN DECIMALS AND LESS THAN 1 ARE UJF. ALL OTHER CAPACITORS ARE PF. ALL INDUCTORS ARE IN μH.



- ▲ MIN. BETA 100. Q1202 AND Q1203 TO BE MATCHED BETA WITHIN 10%.
- ▲ THESE VOLTAGES WILL VARY IN THESE RANGES DEPENDING ON COMPONENT PARAMETERS AND SETTING OF R1208 AND R1204.
- 3 WAVIFORMS ARE FOR APPROXIMATE PHASE RELATIONSHIP ONLY.
- 4 VOLTAGES IN PARENTHESES ARE WITH BOTH CHANNELS DRIVER AT 1 KHZ @ 50W RMS INTO 8 OHM LOAD.

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FIGURE 17B - DRIVER CONDUCTOR DIAGRAM



- SIGNAL PATH
- FEEDBACK PATH
- DIFFERENTIAL SIGNAL VOLTAGE

THE PARTS LIST FOR THIS UNIT IS LOCATED ON PAGE 84.

FIGURE 18A - POWER AMP SCHEMATIC DIAGRAM

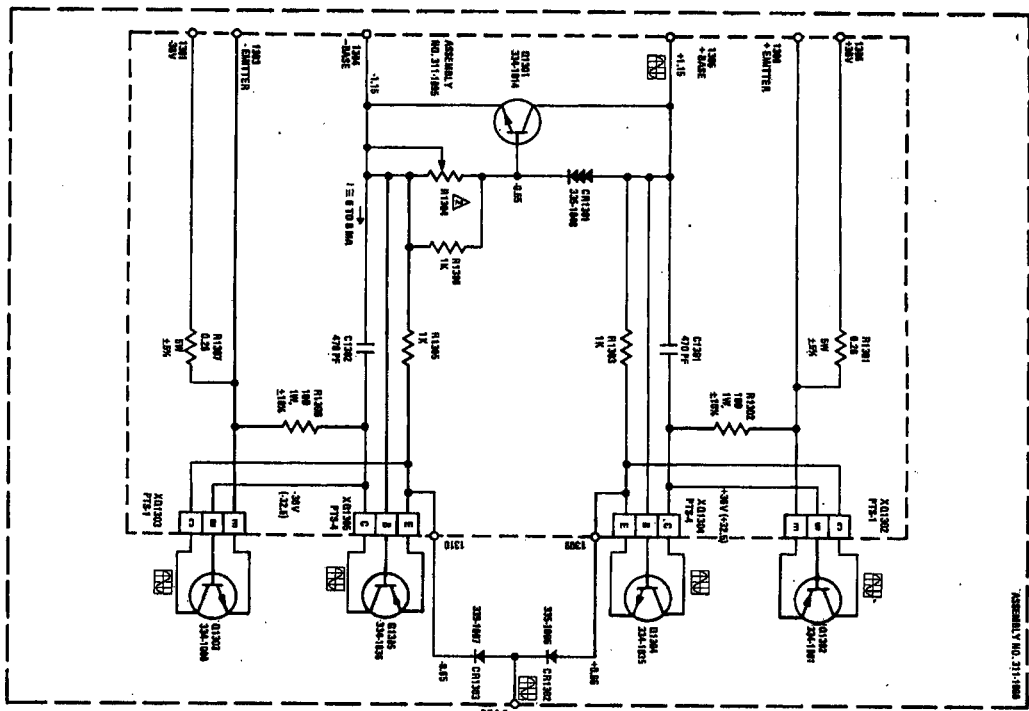


FIGURE 18B - POWER AMP CONDUCTOR DIAGRAM

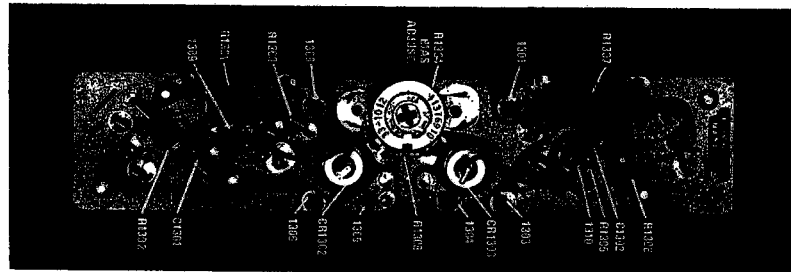
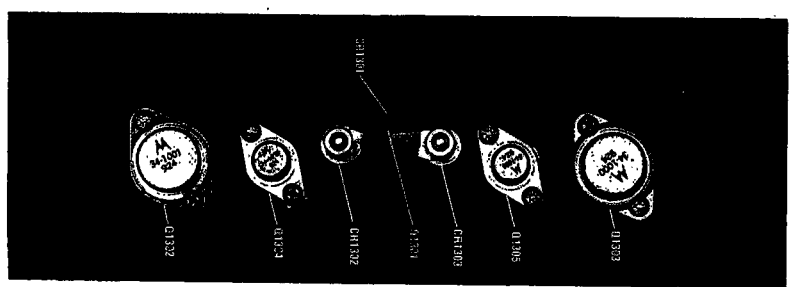


FIGURE 18C - POWER AMP COMPONENT DIAGRAM

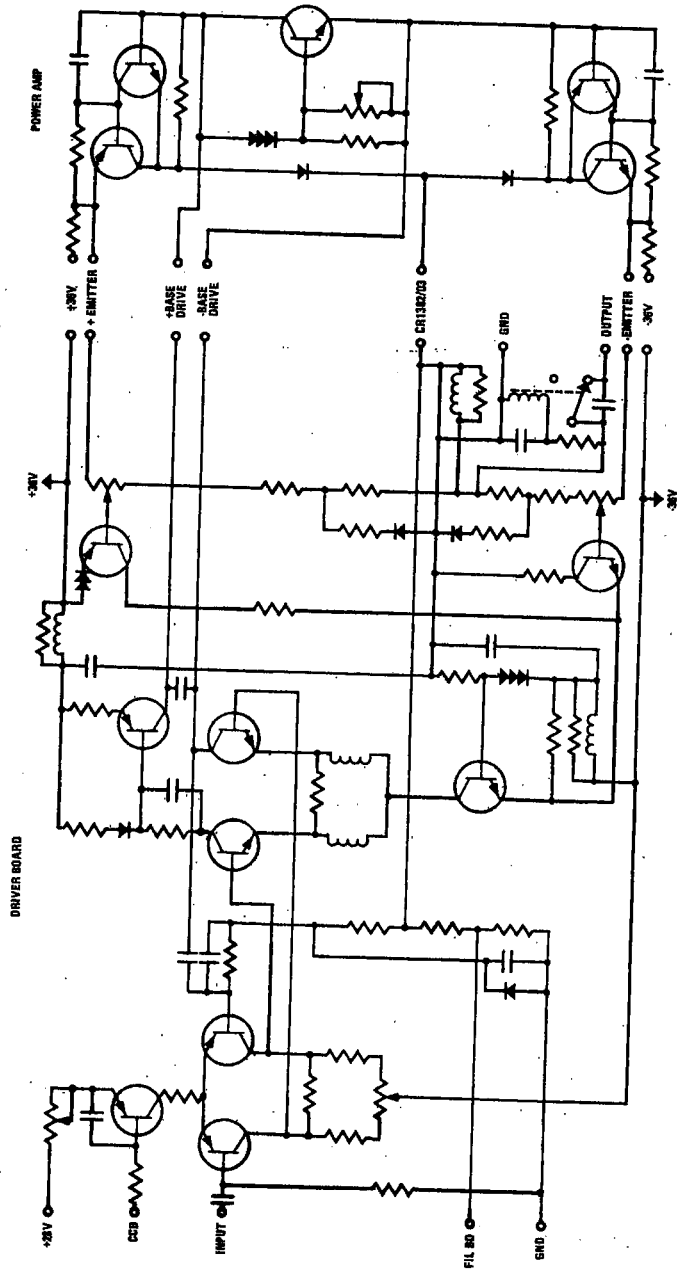


ALL VOLTAGES SHOWN ARE $\pm 10\%$ (UNLESS OTHERWISE NOTED) TAKEN WITH DC VTM PLACED IN AC VOLTAGE RANGE AND OHMS AND AC VOLTAGE RANGE AND IMPEDANCE OF 1 MEGOHM. LINE VOLTAGE EQUALS 120 VAC AT 60 HZ.

UNLESS OTHERWISE NOTED: OHMS $\times 10^3$ ALL CAPACITORS IN DECIMALS AND LESS THAN 1 ARE μF ALL OTHER CAPACITORS ARE PF *ALL INDUCTORS ARE IN μH

- 1 R1004 IS 25K $\pm 20\%$ W.W. LINEAR TAPER, WITH A 220Ω 1.50% STOP ON CW END.
- 2 VOLTAGES IN PARENTHESES ARE WITH BOTH CHANNELS DRIVER AT 1 KHZ @ 50W RMS INTO AN 8 OHM LOAD.
- 3 WAVEFORMS ARE SHOWN FOR APPROXIMATE PHASE RELATIONSHIP ONLY.
- 4 IDLING CURRENT IS ≈ 77 MA.

THE PARTS LIST FOR THIS UNIT IS LOCATED ON PAGE 55.



THIS IS A FUNCTIONAL RELATION DIAGRAM OF THE DRIVER BOARD AND THE POWER AMP.

POWER AMPLIFIER ADJUSTMENT (BIAS)

NOTE

MAKE SURE ALL DRIVER BOARD ADJUSTMENTS HAVE BEEN MADE BEFORE PRECEDING WITH THIS ADJUSTMENT.

1. Bias adjustment may be made with or without a load on the speaker outputs.
2. Apply no inputs. Set volume to full C.C.W. (off) and power OFF.
3. Connect a D.C. V.T.V.M. with a 0.1 volt (full scale) range across R1301 or R1307 (R1307 can be connected across on the driver board at pins 1215-violet wire (-) and 1214-gray wire (+) near top of board.
4. Adjust respective bias pots R1304 for 20 mV. on D.C. V.T.V.M.
5. Check distortion at 20 KHz. It must be less than 0.15%.