

Some Further References Related to the Boxall, Larson, Baxandall-Shallow, and Thompson Constant Current Circuits:

Earlier posts to this “Baxandall Super Pair” thread offered a chronological annotated list of references. It will be seen that this thread name actually applies to the ***Baxandall-Shallow constant current source*** (#5, below). The updated reference list below expands on this theme, with several key historical items added. As previously, some comments on the first few of the references follow. When carefully read, the comments should help in understanding this most clever circuit and the many variations.

Before any discussion, it is worthwhile noting the pros and cons of these bipolar transistor circuit developments, either as it might apply to an amplifying stage, or to a constant current source (CCS). On the pro side, there are improvements seen in the distortion of an amplifying stage, as well as an increase in the output impedance, particularly with regard to frequency. On the con side, these circuits do not address circuit linearity and/or output impedance, as they might be limited by the Early effect.

In #1 and 2, Frank S. Boxall describes a form of feedback using a compound PNP transistor connection which, upon some analysis, can be seen to have improvement properties similar to the Baxandall-Shallow circuit of 1966. Boxall's "Base Current Feedback" can also be described as a re-circulation of the transistor base current back to the emitter. It should be understood that Boxall's feedback is not a form of global feedback around an amplifier, the latter as introduced by Harold Black of Bell Labs.

Boxall's work predated Baxandall-Shallow by nearly a decade, and the context is actually more concerned with reducing distortion of low quality PNP output drivers in communications applications. In the patent document (#1), it does present some challenges to complete interpretation of the intended biasing (Fig. 5). The document includes expressions for distortion reduction and output impedance increase, as resulting from the feedback. In #2, Boxall further clarifies the biasing issues, showing AC-coupled feedback in Fig. 8, and in Fig. 9, presents a DC-coupled circuit which is functionally equivalent to that of the Baxandall-Shallow circuit (albeit of a NPN/PNP format).

The Roger Webster article, #3 within a Texas Instruments Communications handbook, summarizes Boxall's base current feedback methods, and reiterates the expressions for distortion reduction and output impedance increase. Webster references a work by Aldridge, which also addresses distortion reduction techniques.

In #4, Lester L. Larson provides an analysis of a composite PNP/NPN complementary pair as a functional high-gain NPN, with two such pairs operated differentially, or push-pull. The impedance characteristic of the output stage composite device is seen to be improved by a factor of β , the gain of the driving transistor. Larson's work was independent of Baxandall-Shallow, and was part of the development of a push-pull driver for CRT displays.

In #5, Peter J. Baxandall and E. W. Shallow discuss a single current source stage of the PNP/NPN type, having improvements similar to that of Boxall and Larson with regard to output

impedance. This is in fact the seminal paper that led to this “Baxandall Super Pair” thread. In a follow-on letter, #6, Baxandall describes a key point of the Baxandall-Shallow current source: "The effect of collector-base capacitance in Tr1, which shunts the output in the Fig. B circuit, is degenerated in the Fig. C circuit, and output capacitance values of well under 1 pF are obtained." This point is what allows this type of stage (Boxall, Larson, or Baxandall-Shallow) to extend the effective bandwidth of a current source. Simply stated, the output capacitance of the transistor used is effectively reduced, as seen at the collector or output node.

In #7, Jim Thompson describes an op amp with a Figure 6 NPN/PNP current source, designed to overcome the PNP IC transistor β limitations. This NPN/PNP configuration is a functional complement akin to 1/2 the Larson and to the Baxandall-Shallow setups. It was to be used many times over in other Motorola ICs of the period, in addition to discrete circuit examples within applications.

In #8, Jim Solomon offers a detailed analysis on the use of a composite complementary NPN/PNP pair ala Thompson (above), as a functional high-gain PNP pair, within the front end of what became the MC1556 IC op amp.

In #9, Tom Frederiksen describes use of the Figure 6 Thompson composite complementary pair within a high powered voltage regulator IC.

In #10 the Thompson composite complementary pair is used in Figures 9 and 10 as a current mirror employing 2N3904/3906 discrete transistors. In #11 the Thompson composite complementary pair is used in Figures 3 and 4 as a current mirror employing 2N3904/3906 discrete transistors.

In #12, Maurice Free describes the MC1595 multiplier design, which uses externally the Thompson current source scheme, within an output stage current mirror (Figs. 3 and 4).

Credits: For this July 2012 update, I am indebted to several DIYAudio friends for bringing my attention to the Boxall references. Brad Wood, Dmitri Danyuk, and Samuel Groner were all most helpful here. Brad, writing as bcarso, delineated a lot of background on tracking down the Boxall and Larson references (see also <http://www.diyaudio.com/forums/solid-state/166306-origins-baxandall-super-pair.html>). As per John Addis, Larson's Tektronix co-worker, Brad reports that the Larson output stage connection was internally known as the "super-alpha".

Earlier, my thanks went also to Jim Thompson for help with his MSEE thesis (#7), and to Maurice Free for help with his MSEE thesis (#12). Thanks also to an anonymous friend for providing the Larson reference, and to Ben Duncan and Morgan Jones, who also provided several references and other background information. Bob Pease has also related his independent development of this type of circuit, in #19 and 21.

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1. F. S. Boxall, “Feedback Amplifier Circuit”, **US Patent 2,960,660**, filed June 7,

1957, issued Nov 15, 1960.

2. F. S. Boxall, "Base Current Feedback and Feedback Compound Transistor", *Semiconductor Products*, Vol. 1, no. 5, pp. 17-24, September-October, 1958.
3. Roger Webster, "Transistors in Wide-band Low-Distortion Amplifiers", within *Communications Handbook Part II*, 1965, by Engineering Staff of Texas Instruments Incorporated; John R. Miller, Editor. Further reading: E. E. Aldridge, "Engineering Treatment of Transistor Distortion", *IRE Trans. On Circuit Theory*, vol. CT-9, p. 183, June, 1962.
4. L. L. Larson, "Differential Amplifier Having Common Base Output Stage of Very High Impedance", **US Patent 3,394,316**, filed Jan 29, 1965, issued July 23, 1968.
5. P. J. Baxandall, E.W. Shallow, "Constant Current Source With Unusually High Internal Resistance And Good Temperature Stability," *Electronic Letters*, Sept. 1966, Vol. 2, No. 9, pp. 351-352.
6. P. J. Baxandall, "Constant-Current Circuits," *Wireless World (Letters)*, Dec. 1966, pp. 609.
7. James Elbert Thompson, "A High Performance Operational Amplifier Utilizing Field Effect Input Devices Compatible With Integrated Circuit Fabrication Techniques", *MSEE Thesis, Arizona State University*, June, 1968. See also http://analog-innovations.com/MS_Thesis_JE_Thompson_1968.pdf
8. J. E. Solomon, "Lateral PNP-NPN Composite Monolithic Differential Amplifier", **US Patent 3,538,449**, filed Nov 22, 1968, issued Nov 3, 1970.
9. Thomas M. Frederiksen, "A Monolithic High-Power Series Voltage Regulator," *IEEE Journal of Solid-State Circuits*, Dec 1968, Vol. 3, #4, pp. 380-387.
10. Ed Renschler, "Analysis and Basic Operation of the MC1595," *Motorola Semiconductor Products, Multiplier Series Part I, Application Note AN489*, September 1969.
11. Brent Welling and Loren Kinsey, "Using the MC1495 Multiplier in Arithmetic Operations," *Motorola Semiconductor Products, Multiplier Series Part II, Application Note AN490*, September 1969.
12. Maurice George Free, "An Integrated Linear-Transconductance Analog Multiplier", *MSEE Thesis, University of Arizona*, 1970. See also "An Integrated Linear-Transconductance Analog Multiplier", *Simulation*, Vol. 13, #5, November 1969, pp. 243-251.
13. "MC1594L/MC1494L Data Sheet, Figure 17," *Motorola Semiconductor*

Products, October 1970.

14. Allan Grebene, section 4-5, pp. 133-136, 143-144, within **Analog IC Circuit Design**, Van Nostrand Reinhold, 1972, ISBN 0-442-22827-9.

15. Hans R. Camenzind, 'Voltage-to-Current Converter' section, pp. 266-269, within Chapter 16 'Linear Elements, Circuits, and Subsystems' of **Electronic Integrated Systems Design**, Van Nostrand Reinhold, 1972.

16. R.C. Jaeger, "A High Output Resistance Current Source," *IEEE Journal of Solid-State Circuits*, Aug 1974, Vol. 9, # 4, pp. 192-194.

17. B. Hart, "Homage To Baxandall," *Electronics World (Letters)*, Jan. 2003, pp. 41.

18. N. Terzopoulos, K. Hayatleh, B. Hart, F. J. Lidgley and C. McLeod, "A Novel Bipolar-Drive Circuit for Medical Applications," *Physiological Measurement Journal*, Issue 5, N21-N27, October 2005.

19. Bob Pease, "What's All This PNP Stuff, Anyway?," *Electronic Design*, Sept. 11, 2008, pp. 80. See also <http://electronicdesign.com/Articles/ArticleID/19605/19605.html>

20. Dimitri Danyuk, "On the Optimization of Enhanced Cascode," Preprint #7571, Presented at the 125th AES Convention, October 2-5, 2008 San Francisco, CA, USA.

21. Bob Pease, "Mailbag; letters from Walt Jung and James E. Thompson", *Electronic Design*, October 23, 2008, pp. 72. See also <http://electronicdesign.com/Articles/Index.cfm?AD=1&ArticleID=19868>