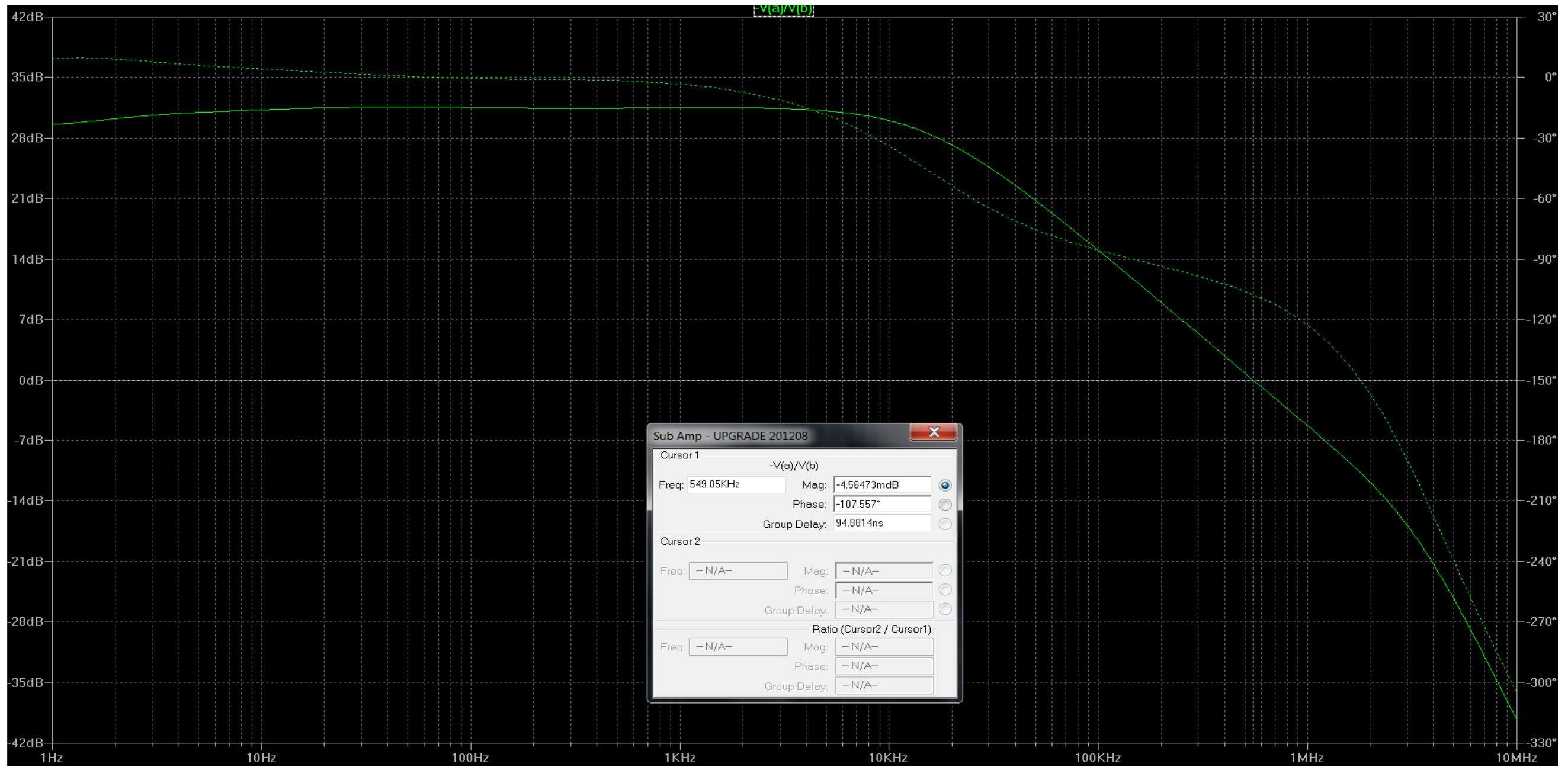


Change can be seen in red circle.  
 Resistive loading of VAS is changed to local feedback to increase open loop bandwidth

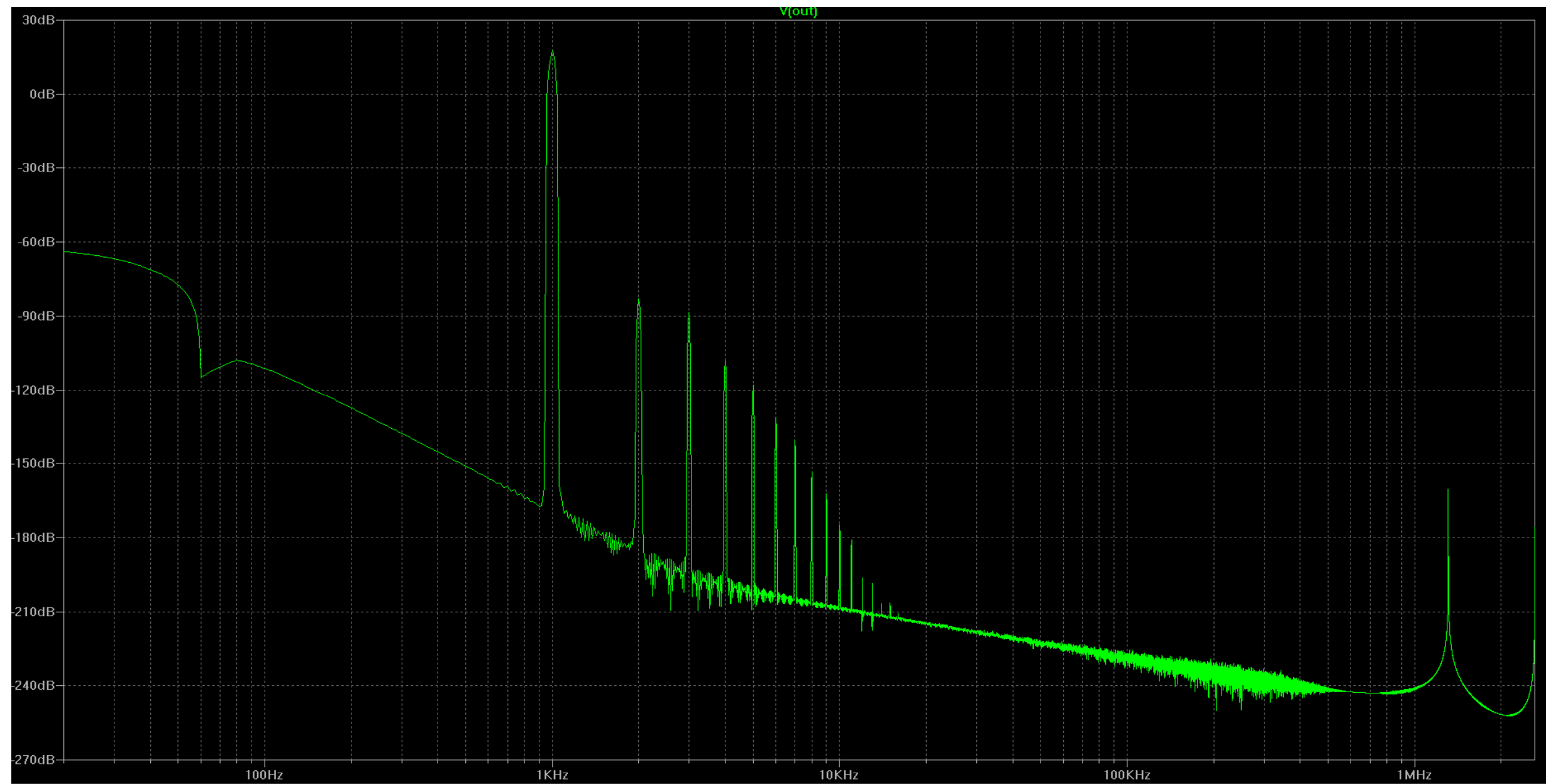


Open Loop AC analysis

Phase margin  
Gain Margin

70°  
-12dB

FFT 0,4Vin 11V peak out

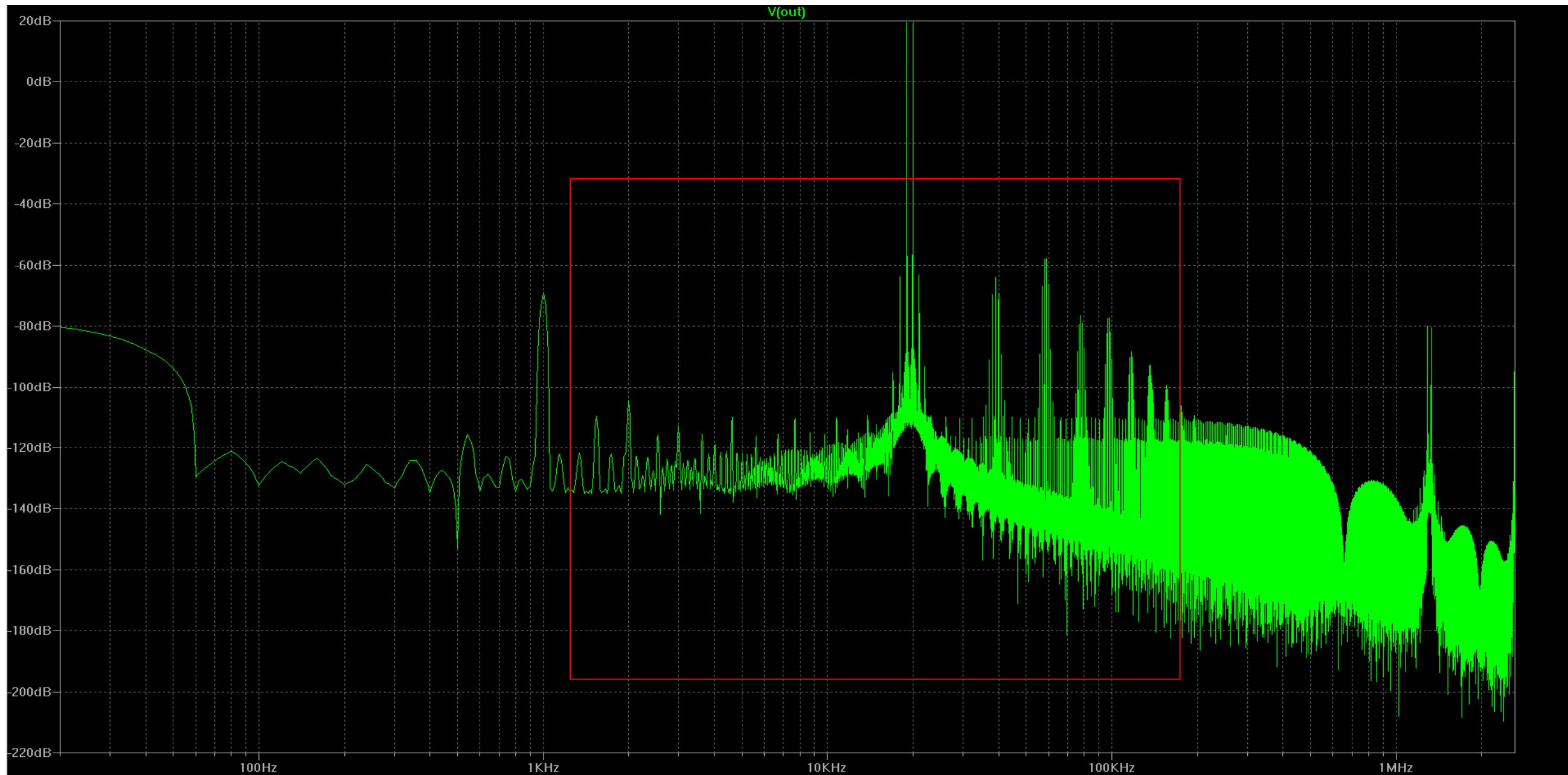


Fourier components of V(out)

DC component:-0.00864338

Harmonic Number	Frequency [Hz]	Fourier Component	Normalized Component	Phase [degree]	Normalized Phase [deg]
1	1,00E+06	1,09E+04	1,00E+03	-0.24°	0.00°
2	2,00E+06	1,11E-01	1,02E-02	150.67°	150.91°
3	3,00E+06	4,66E-02	4,28E-03	-33.67°	-33.43°
4	4,00E+06	2,94E-04	2,70E-05	-110.05°	-109.81°
5	5,00E+06	4,34E-03	3,99E-04	157.05°	157.29°
6	6,00E+06	4,07E-03	3,73E-04	176.28°	176.52°
7	7,00E+06	3,35E-03	3,07E-04	-178.36°	-178.12°
8	8,00E+06	2,84E-03	2,61E-04	-179.38°	-179.13°
9	9,00E+06	2,52E-03	2,31E-04	179.85°	180.09°

Total Harmonic Distortion: 0.001105%



CCIF 19Khz + 20Khz  
Input voltages: 0,5V

See next page for detail of red square

