



Step 1: measurement of noise in B20k at the output of OP1 with S1 closed and S2 opened, resulting in a total gain of G2 and an output voltage of $e_{N.out.1}$
 Step 2: decrease of circuit gain to G1 by opening of S1 and closing of S2
 Step 3: adjustment of R as long as the test circuit produces the Step 1 output noise voltage $e_{N.out.1}$
 Step 4: measurement of R: this will be the value of r_{bb} of the DUT
 For NPN reverse Voltage