



The diagram shows a circuit labeled "new buffer circuit". It features a U3a DIL TLC2272 op-amp. The non-inverting input (IN+) is connected to a voltage divider consisting of a 1K resistor and a 470K resistor, with a 100nF capacitor in parallel with the 1K resistor. The inverting input (IN-) is connected to the output (OUT) through a 10uF capacitor. The op-amp is powered by a +5V supply and ground (Vss). The output (OUT) is connected to a +5V supply through a 1N4148 diode and to ground through another 1N4148 diode. The output is also connected to a C11 capacitor, which is then connected to a 1K resistor and a 100K resistor to ground. The final output is labeled "LINE OUT".