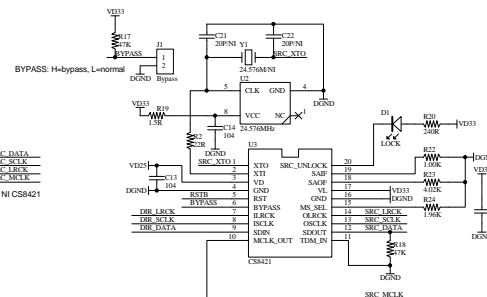


CP00	SDOUT	AB
0	0	125
0	1	126
1	0	127
1	1	128

ERR: 0=Slave, 1=Master  
CSB: 0=RoTx, 1=ZS=Tx

DR_DATA	DR_CLK	DR_LCK	DR_MCK
0	0	0	0
0	0	1	0
0	0	1	1
0	1	0	0
0	1	0	1
0	1	1	0
0	1	1	1
1	0	0	0
1	0	0	1
1	0	1	0
1	0	1	1
1	1	0	0
1	1	0	1
1	1	1	0
1	1	1	1

use when NI CS421



INPUT: R22-2K GND> L-J up to 32bit  
R23-64K GND> L-J 24bit  
OUTPUT: R24-2K GND> IN-Slave, OUT=Master 12bit

