

Title		
Size	Number	Revision
A4		
Date:	22/04/2012	Sheet of
File:	C:\s3rup\Altium\TDA1541_DAC\Top.SchDoc	Drawn By:

1

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A

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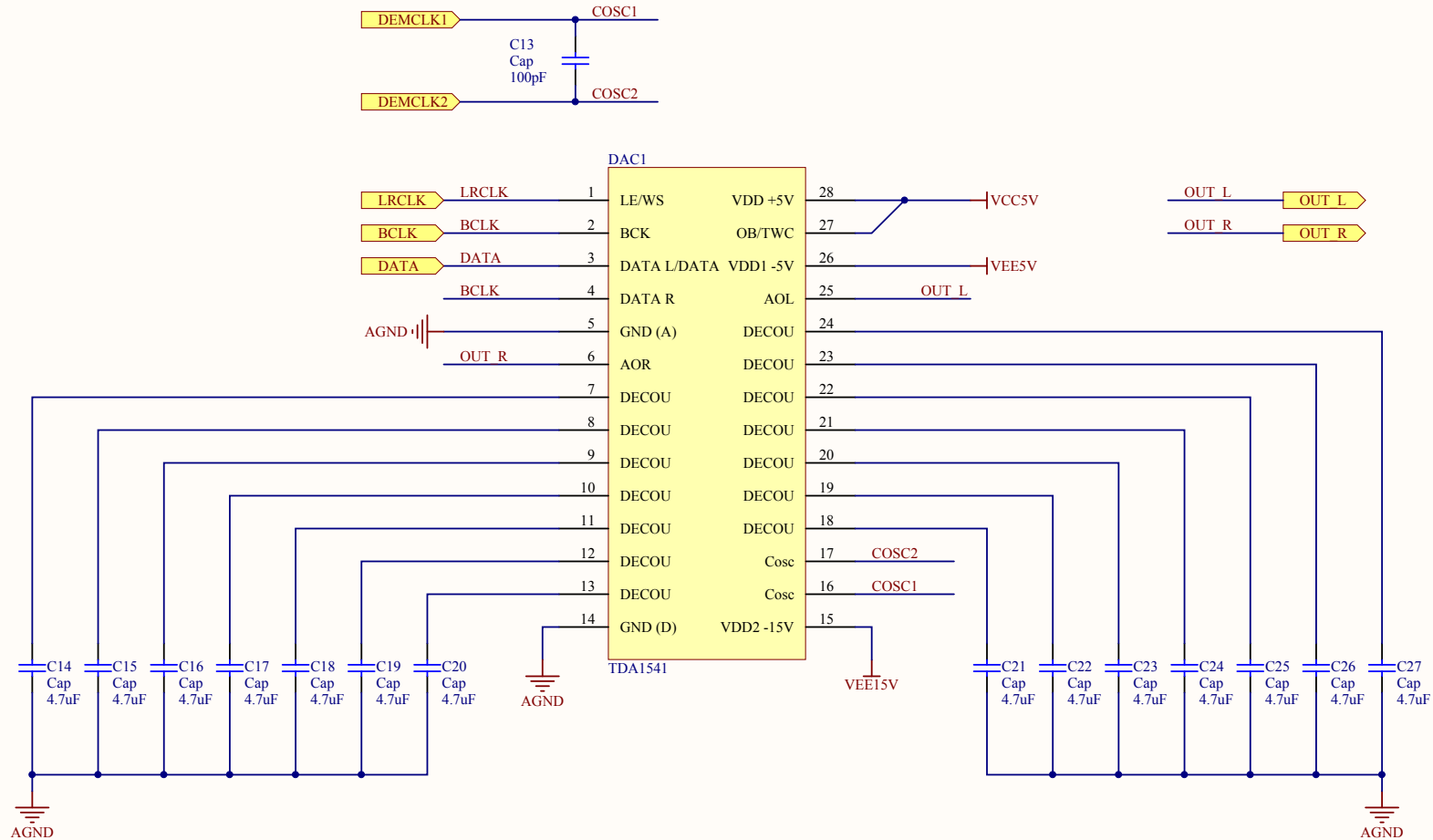
B

C

C

D

D



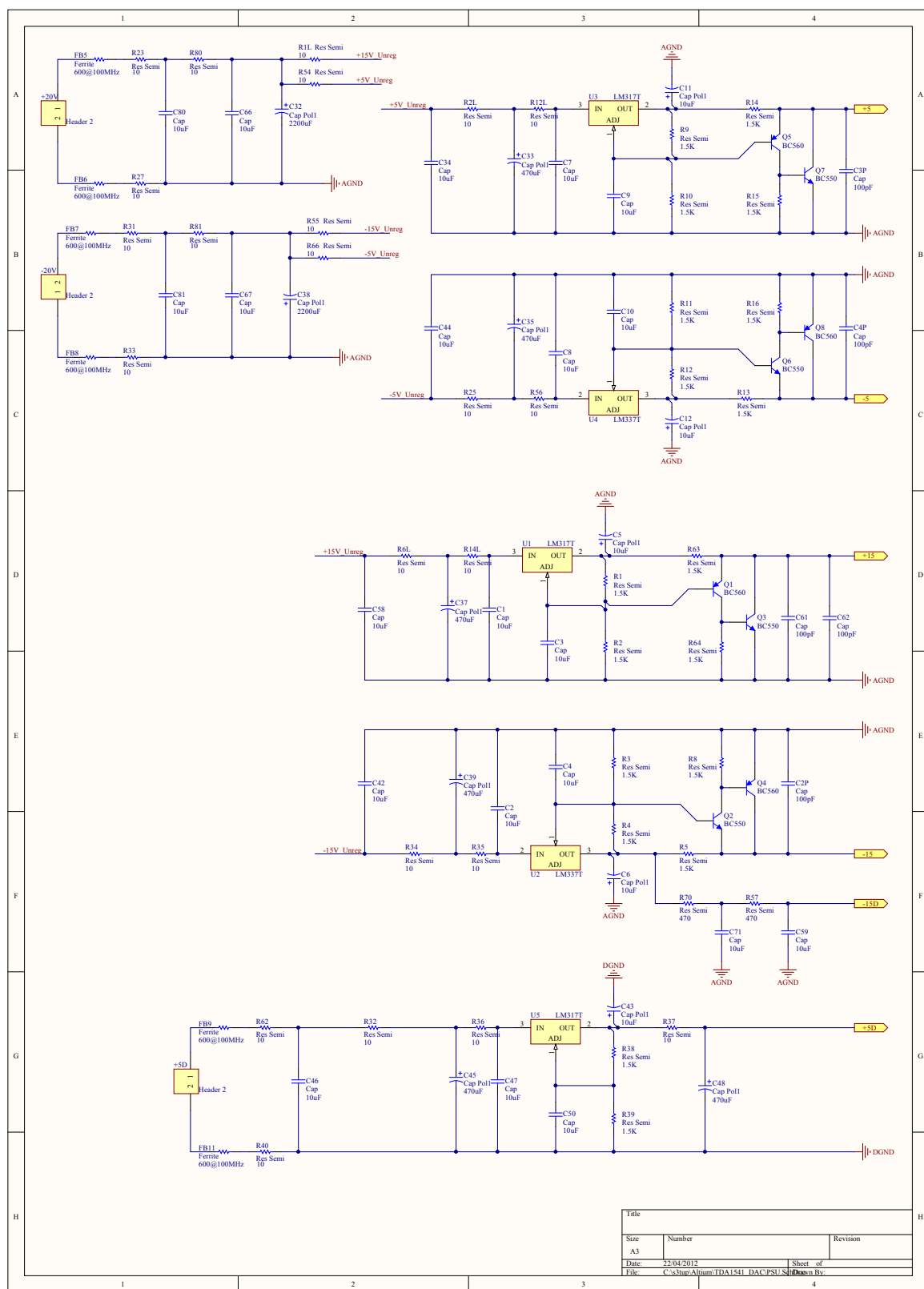
Title		
Size	Number	Revision
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Date:	22/04/2012	Sheet of
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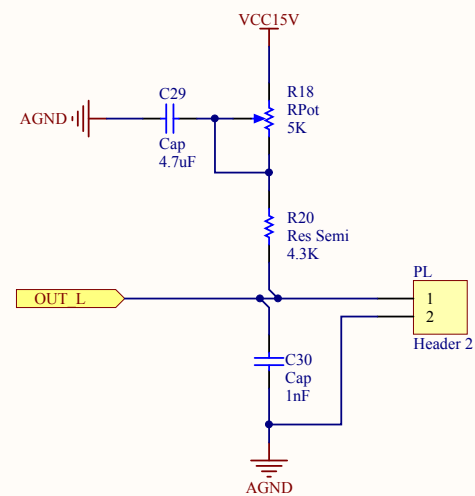
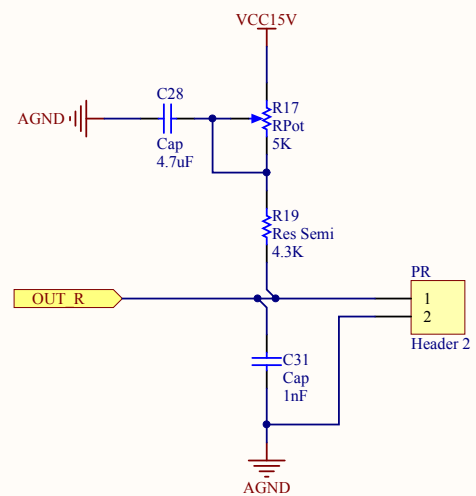
2

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Title		
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Title		
Size A4	Number	Revision
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File:	C:\s3rup\...\Current Bias.SchDoc	Drawn By:

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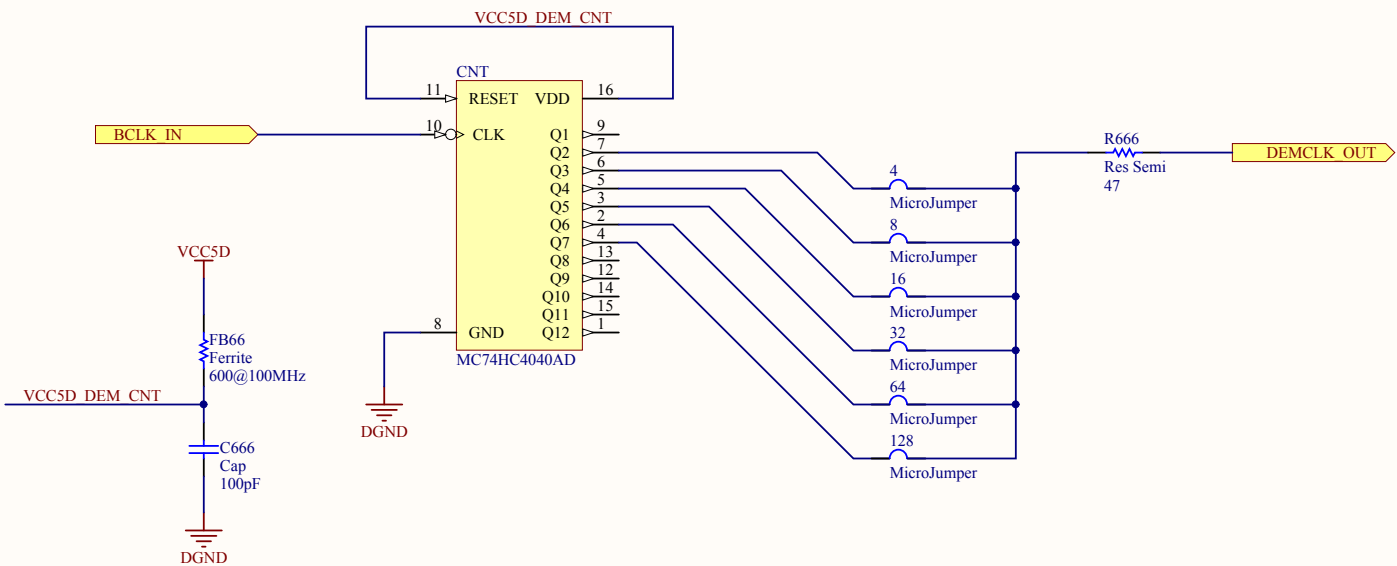
B

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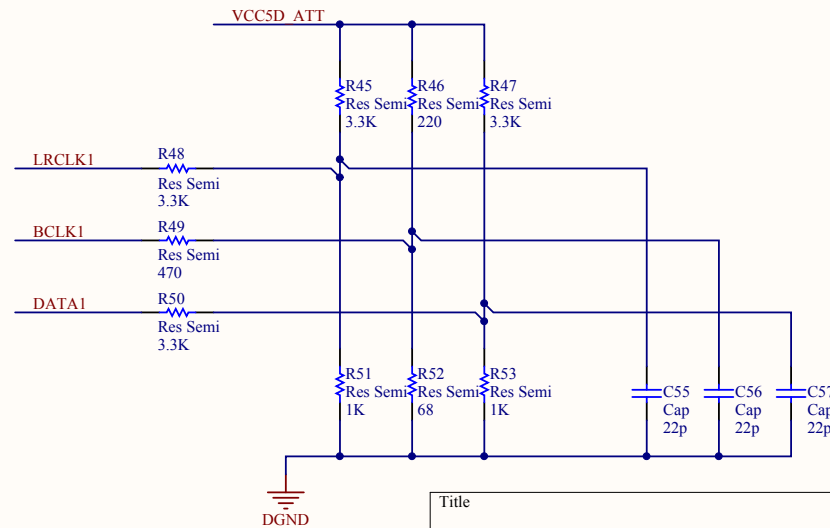
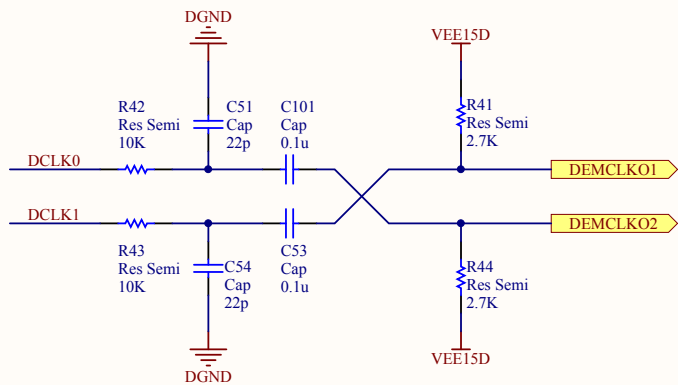
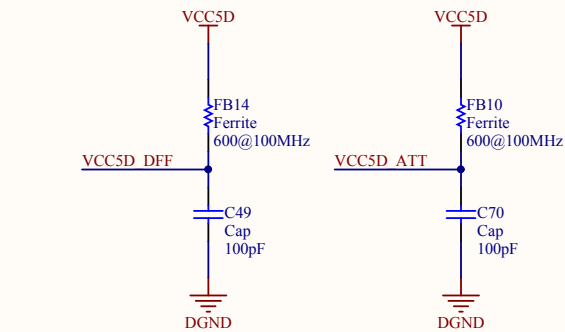
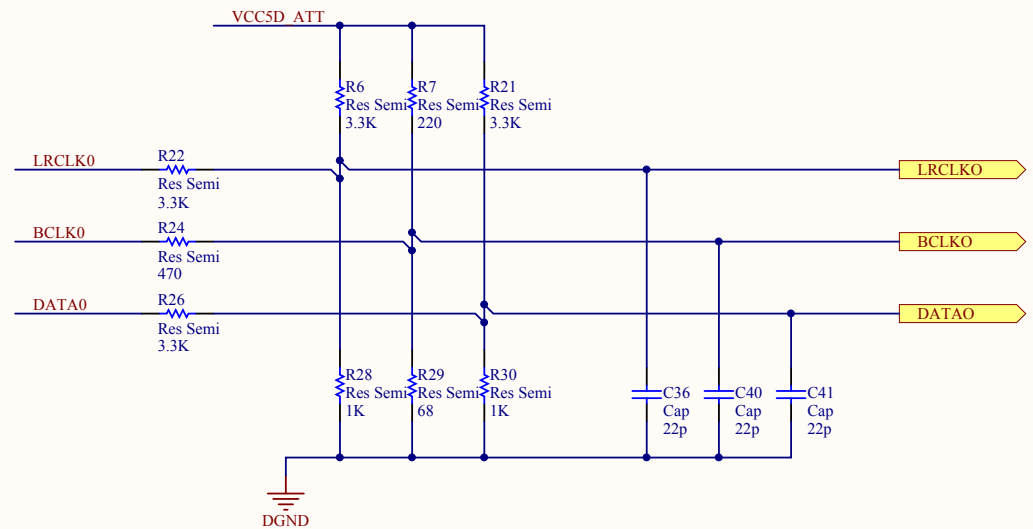
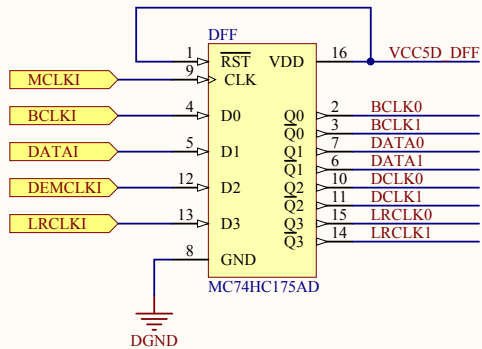
Title		
Size A4	Number	Revision
Date:	22/04/2012	Sheet of
File:	C:\s3rup\...\DEM Clock Divider.SchDoc	Drawn By:

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Size	Number	Revision
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File:	C:\s3rup\Reclock.SchDoc	Drawn By: