

Only output stage has fuses:
 - Without zener and 1N4148: output FET's are all dead when one or both fuses blown
 - Drivers survive while they keep connected to rails.
 - VGS of output FET's will be to high

```
.param Vin=0.15
.options plotwinsize=0
.options numdgt=7
.param Freq=1000Hz
.param numcyc=500
.param dlycyc=5
.param FFT=65536
.param simtime=(dlycyc+numcyc)/Freq
.param dlytime=dlycyc/Freq
.param numsample=simtime/Freq/((simtime/numcyc)*FFT)
.tran 0 (simtime) (dlytime) (numsample)
.ac dec 30 1 10meg
.noise V(out) V5 dec 100 :1 1000meg
```

