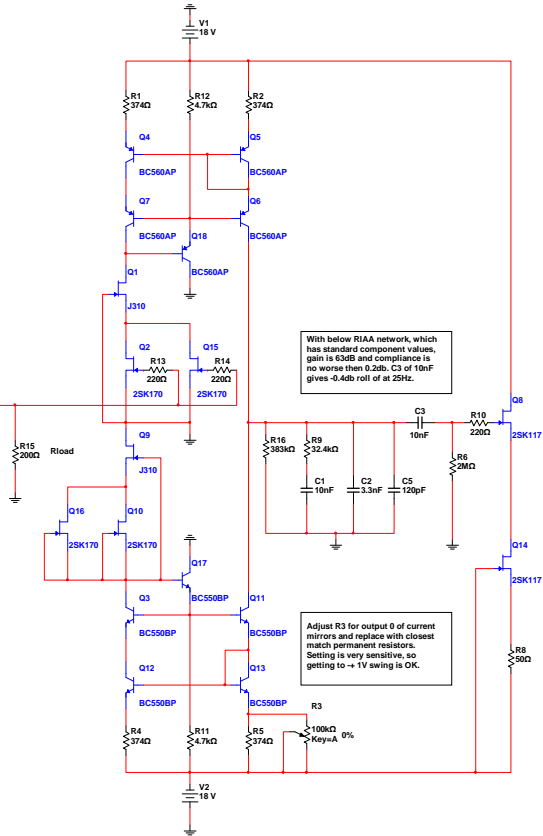
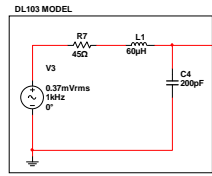
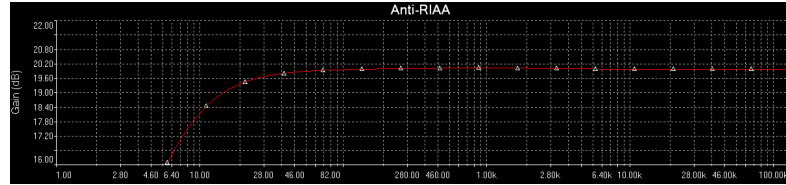


NOTE: Before connecting sources of input jfets (Q2, Q15) to ground, ensure there is no more than 1mV dc offset. Degenerate by adding source resistor to each jfet for upper (if offset is positive) or lower (if negative) pair. Circuit must reach thermal balance, so let it settle for couple minutes before taking any measurements. Replace gate stoppers (R13, R14) with lowest value that you can get away with, without turning this circuit to AM receiver.



With below RIAA network, which has standard component values, gain is 63dB and compliance is no worse than 8.2db. C3 of 10nF gives -9.4db roll off at 25Hz.

Adjust R3 for output 0 of current mirrors and replace with closest match permanent resistors. Setting is very sensitive, so getting to $\pm 1V$ swing is OK.



Fourier analysis for VC23:					
1	DC component:	0.00705485			
2	AC Harmonics:	0			
3	THD:	0.0147089 %			
4	THD:	255			
5	Interpolation Degree:	1			
6					
7					
8	Harmonics:	Frequency	Magnitude	Phase	Norm. Mag
9	1	1000	0.009420	-48.932	1
10	2	2000	0.001150e-005	-133.51	0.000147052
11	3	3000	0.000420e-006	-104.11	5.16675e-006
12	4	4000	0.000420e-007	-146.19	5.16675e-006
13	5	5000	0.000420e-007	-130.65	5.16675e-006
14	6	6000	0.000420e-007	-132.69	5.16675e-006

