

CD audio decoder, digital servo and filterless
DAC with integrated pre-amp and laser control

SAA7824

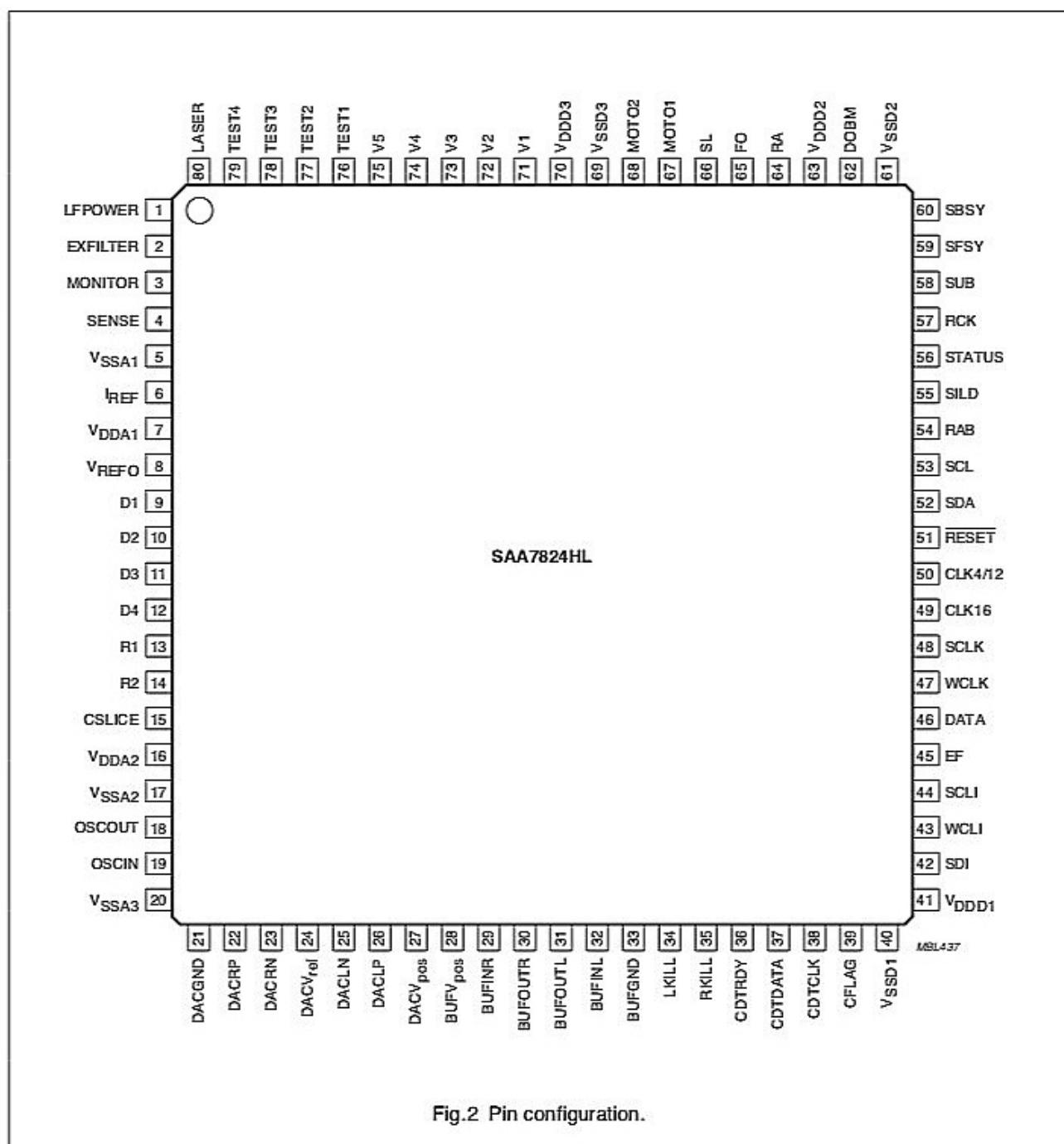


Fig.2 Pin configuration.

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7.10.1.2 Loopback external data into onboard DAC

The onboard DAC can also be set to accept serial data inputs from an external source, e.g. an Electronic Shock Absorption (ESA) IC. This is known as loopback mode and is enabled by setting shadow register 7 to 0000. This enables the serial data output pins (SCLK, WCLK, DATA and EF) so that data can be routed from the SAA7824 to an external ESA system (or external DAC).

The serial data from an external ESA IC can then also be input to the onboard DAC on the SAA7824 by utilising the serial data input interface (SCLI, SDI and WCLI).

In this mode, a wide range of data formats to the external ESA IC can be programmed as shown in Table 7. However, the serial input on the SAA7824 will always expect the input data from the ESA IC to be 16-bit f_s and the same data format, either I²S-bus or EIAJ, as the serial output format (set by decoder register 3).

7.10.2 EXTERNAL DAC INTERFACE

Audio data from the SAA7824 can be sent to an external DAC, identical to the SAA732x series, in 'loopback' mode (i.e. shadow register 7 is set to 0000).

Table 7 DAC interface formats

REGISTER 3	SAMPLE FREQUENCY	NUMBER OF BITS	SCLK (MHz)	FORMAT	INTERPOLATION
1010	f_s	16	2.1168 × n	CD-ROM (I ² S-bus)	no
1011	f_s	16	2.1168 × n	CD-ROM (EIAJ)	no
1110	f_s	16/18 ⁽¹⁾	2.1168 × n	Philips I ² S-bus 16/18 bits ⁽¹⁾	yes
0010	f_s	16	2.1168 × n	EIAJ 16 bits	yes
0110	f_s	18	2.1168 × n	EIAJ 18 bits	yes
0000	4 f_s	16	8.4672 × n	EIAJ 16 bits	yes
0100	4 f_s	18	8.4672 × n	EIAJ 18 bits	yes
1100	4 f_s	18	8.4672 × n	Philips I ² S-bus 18 bits	yes
0011	2 f_s	16	4.2336 × n	EIAJ 16 bits	yes
0111	2 f_s	18	4.2336 × n	EIAJ 18 bits	yes
1111	2 f_s	18	4.2336 × n	Philips I ² S-bus 18 bits	yes

Note

1. In this mode the first 16 bits contain data, but if any of the fade, attenuate or de-emphasis filter functions are activated then the first 18 bits contain data.

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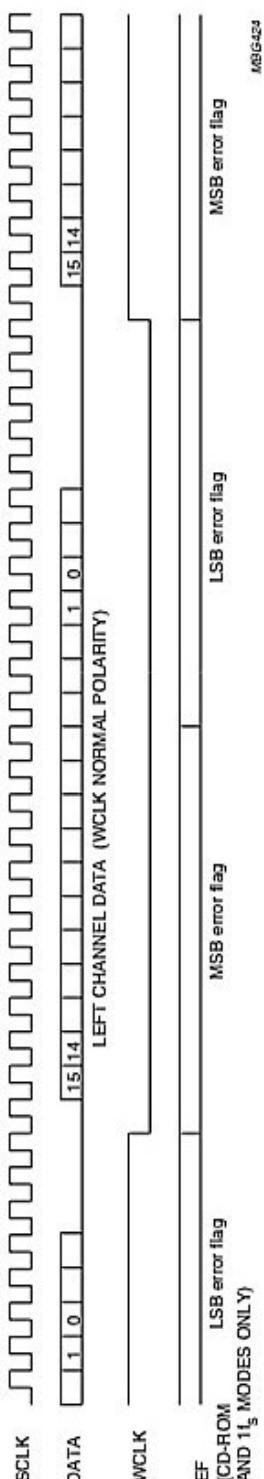


Fig.14 Philips PS-bus data format (16-bit word length).

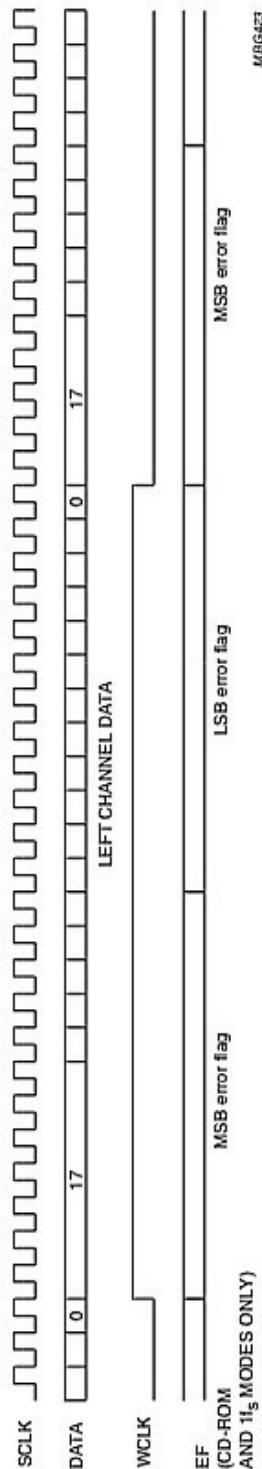


Fig.15 EIAJ data format (18-bit word length).