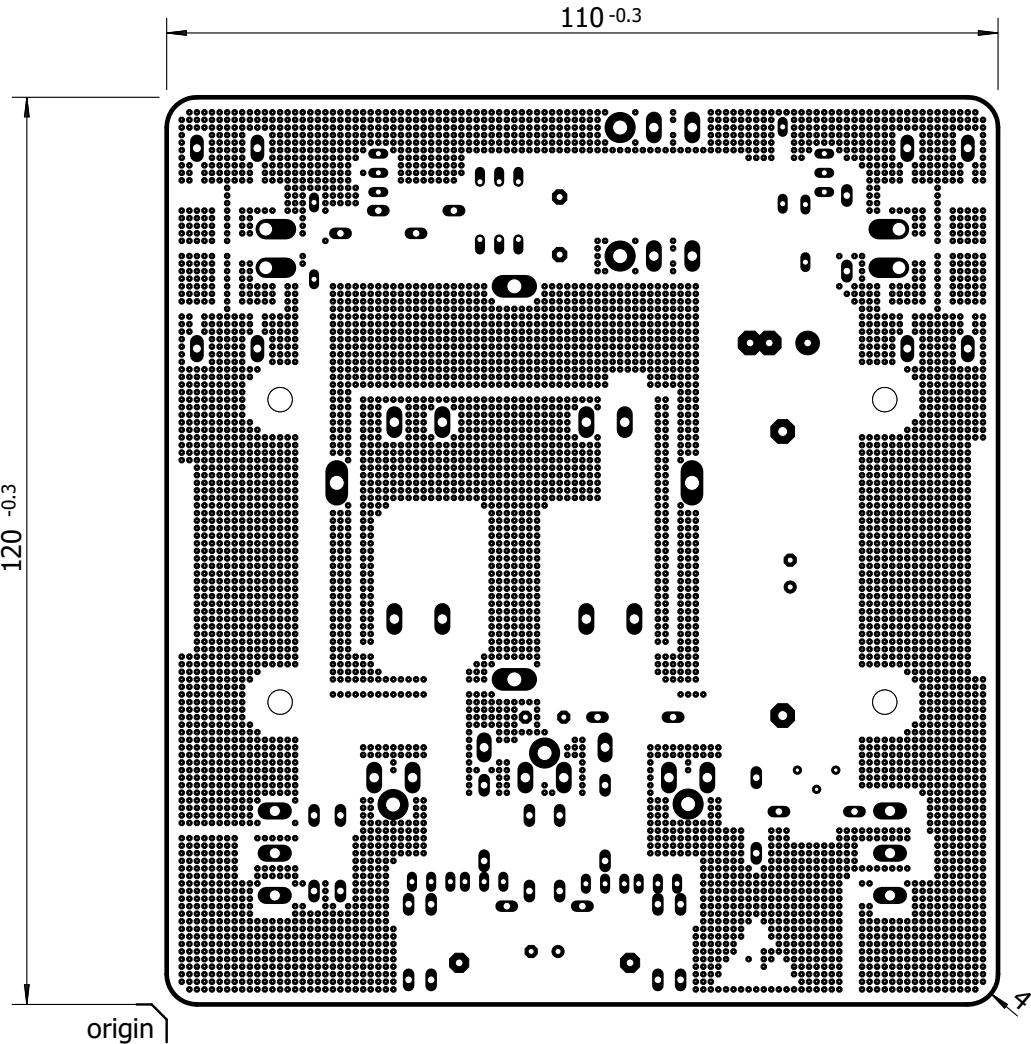


View on top side



Layer stackup:

01		CU 70µm
02	core FR4	CU 70µm

Thickness = 2.0 +/-0.2 mm

REV	IND	DATE	CHANGE HISTORY	ISSUE	DIY Community	THIS DRAWING CONTAINS PROPRIETARY INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION	2017	©	REV
				DRAWN	TITLE Printed Circuit Board				001
				JPS64 21.07/17	FIRSTWATT M2 TRIBUTE				
				CHECKED	DATE 24.07.2017 09:57				DRG N°
				DATE	DIY-NR002				
001		24.07.2017	First iteration		FILE: PAS_M2-CASCODE_FINAL				PAGE: