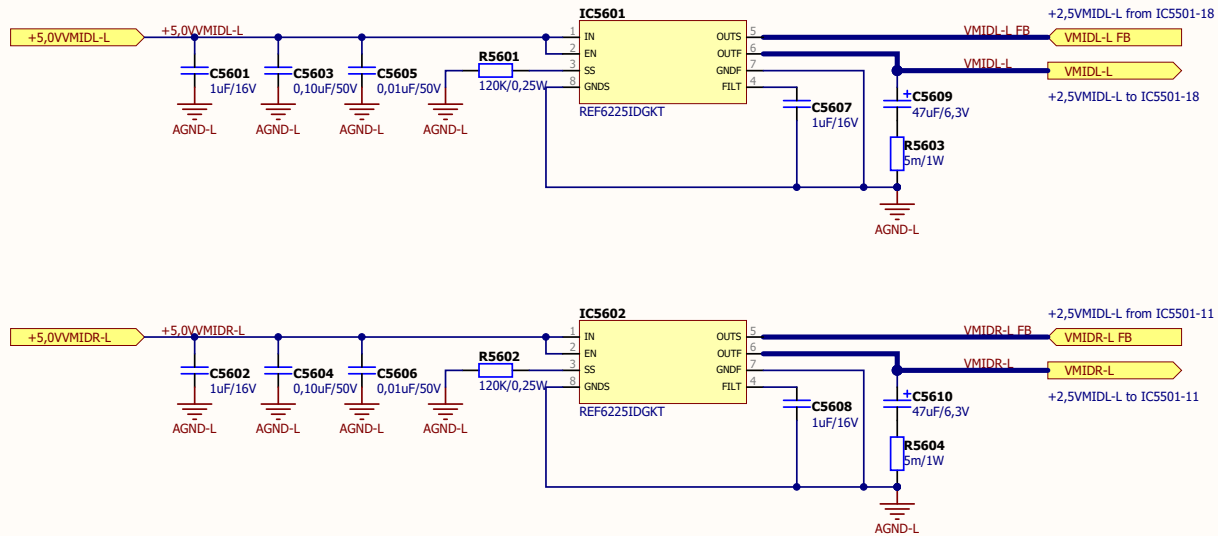


WM8741 DAC V-References Left



Ref. Source:
<http://www.diyaudio.com/forums/digital-source/25213-capcitor-type-vref-3.html>

Left & Right Channels Have Separated Planes

Example of a PCB layout for a data-acquisition system using the REF60xx - see in datasheet. Some key considerations are:

- Connect low-ESR, 0.1-μF ceramic bypass capacitors between the VIN pin and ground.
- Place the REF60xx output capacitor (CL) and the ADC as close to each other as possible.
- Run two separate traces between VOUT_F, VOUT_S and the output capacitor, as shown in Figure 62.
- Short the GND_F and GND_S pins with a solid plane, and extend this plane to connect to the output capacitor CL, as shown in Figure 62.
- Use a solid ground plane to help distribute heat and reduces electromagnetic interference (EMI) noise pickup.
- Place the external components as close to the device as possible. This configuration prevents parasitic errors (such as the Seebeck effect) from occurring.
- Do not run sensitive analog traces in parallel with digital traces. Avoid crossing digital and analog traces if possible, and only make perpendicular crossings when absolutely necessary.

REF6225 - Features:

- Excellent Temperature Drift Performance
 - 3 ppm/°C (max) from 0°C to +70°C
- Extremely Low Noise
 - Total Noise: 5 μVRMS With 47-μF Capacitor
 - 1/f Noise (0.1 Hz to 10 Hz): 3 μVPP/V
- Integrated ADC Drive Buffer
 - Low Output Impedance: < 50 mΩ (0-200 kHz)
 - First Sample Precise to 18 Bits With ADS8881
 - Enables Burst-Mode DAQ Systems
- Low Supply Current: 820 μA
- Low Shutdown Current: 1 μA
- High Initial Accuracy: ±0.05%
- Very-Low Noise and Distortion
 - SNR: 100.5 dB, THD: –125 dB (ADS8881)
 - SNR: 106 dB, THD: –120 dB (ADS127L01)
- Output Current Drive: ±4 mA
- Programmable Short-Circuit Current

Title		
M5100 WM8741 DAC V-Refs Left		
Size	Number	Revision
A4		1.0
Date:	2017-11-29	Sheet 5 of 5
File:	D:\Audio\...\M5600 WM8741 DAC V-Refs Left	Drawn By: JK