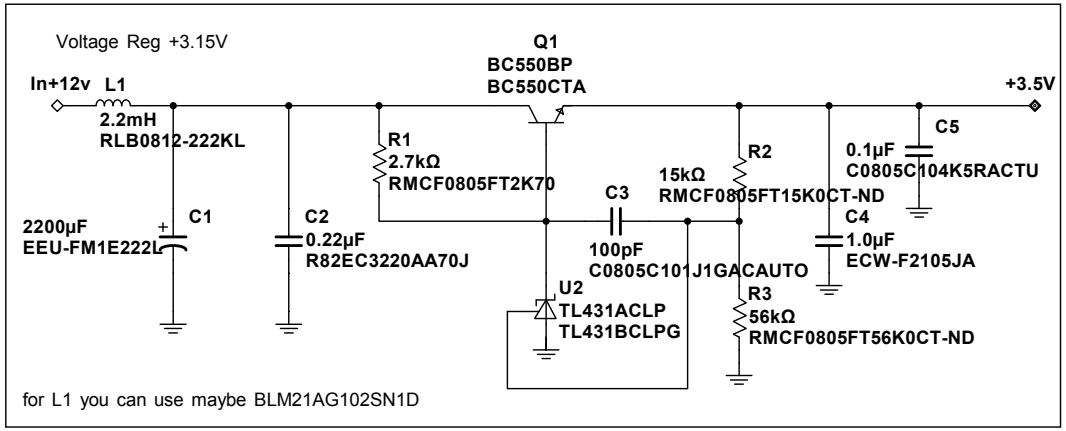

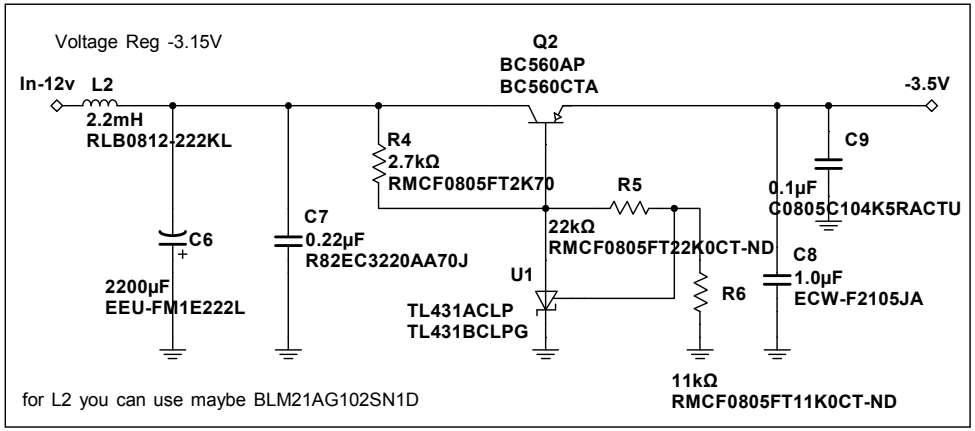

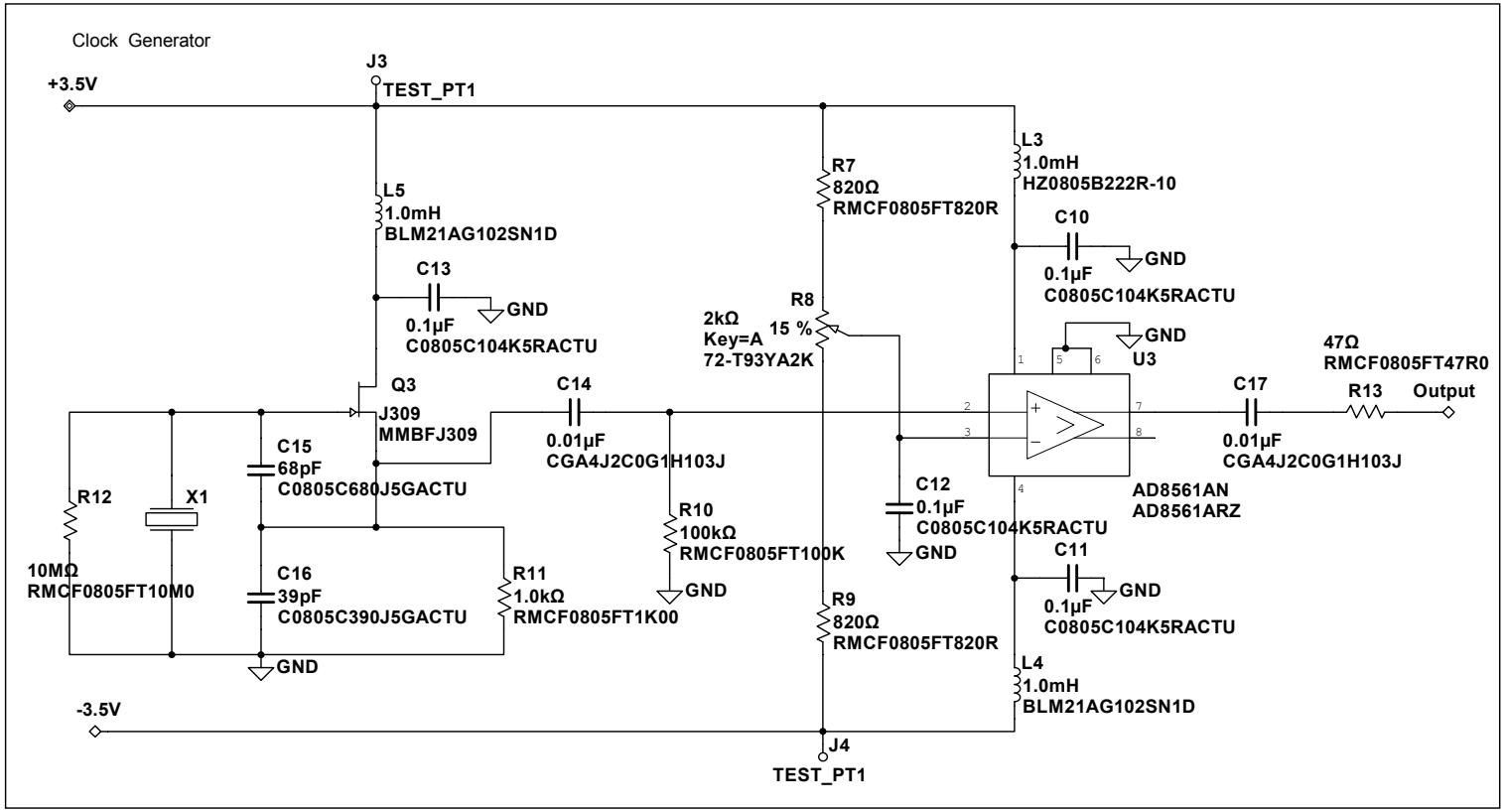

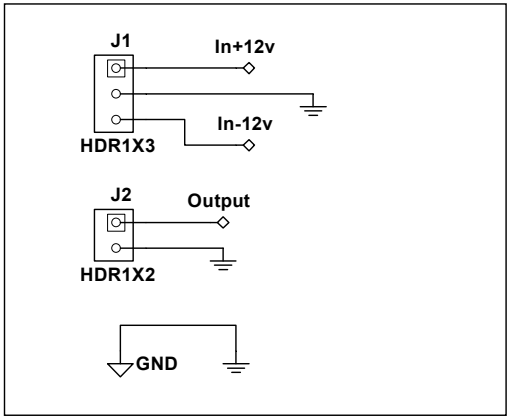

Kwak Clock 7 by Elso Kwak.
Smd circuit design by Michele Adamoli.

With R2, R3, R5, R6 values as above,
the clock high logical level voltage output is
about 2V, instead of classical 5V (LVCMOS).
Tested on Marantz CD5003 cd player
Input voltage: from +- 9V up to +- 16V.

For the original values of resistors, please refer
to the original design by Elso Kwak.

(C) Elso Kwak - Not intended for commerial use.