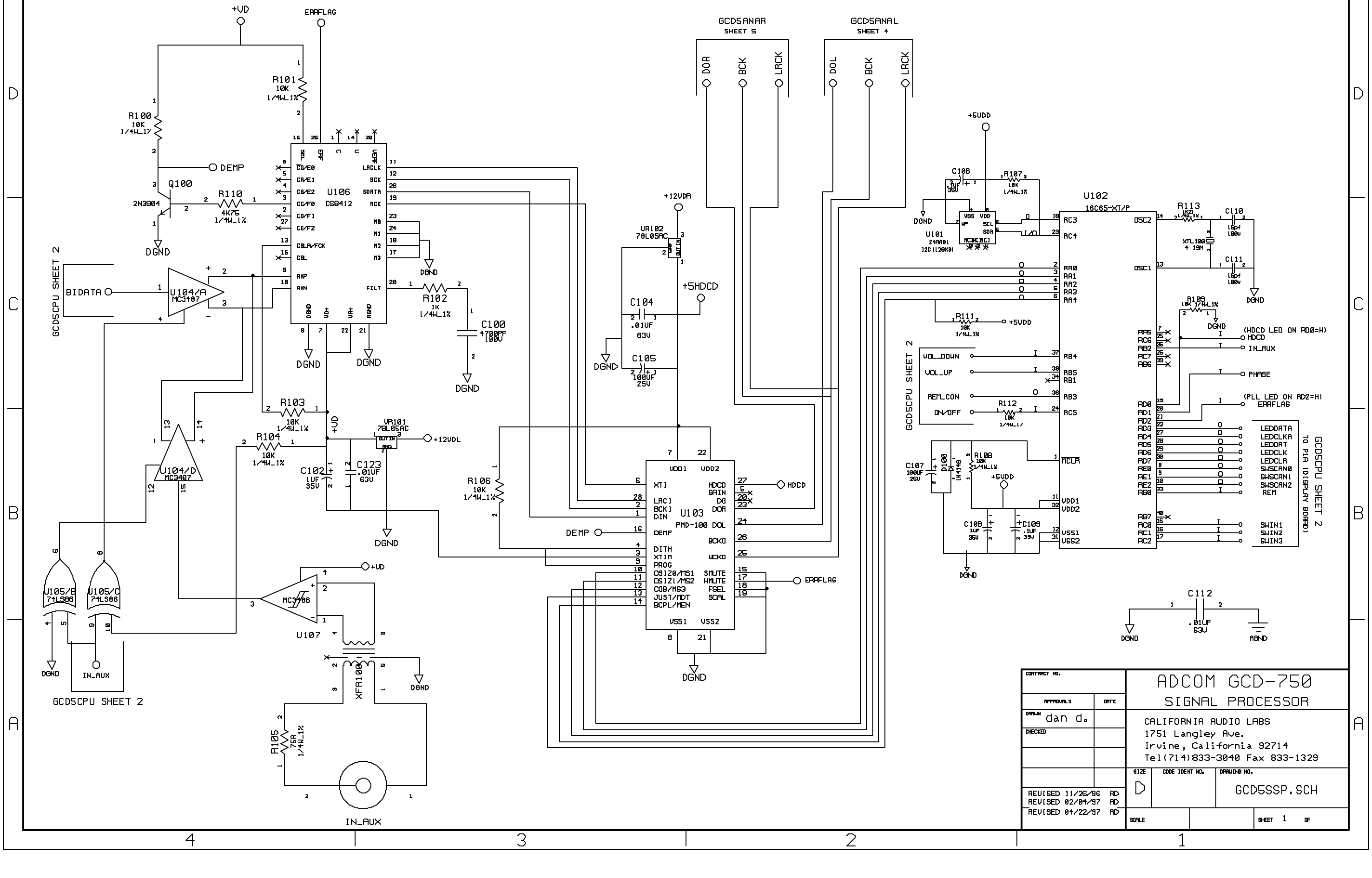
+VD
ERRFLAG
R101
10K
1/4W_1%
R100
10K
1/4W_1%
DEMP
Q100
2N3904
R110
4K75
1/4W_1%
DGND
U106
CS8412
BIDATA
U104/A
MC3487
GCD5CPU SHEET 2
R102
1K
1/4W_1%
C100
470PF
100V
R103
10K
1/4W_1%
R104
10K
1/4W_1%
U104/D
MC3487
VR101
78L05AC
+12VDL
C102
1UF
35V
C123
.01UF
63V
U105/B
74LS86
U105/C
74LS86
U105/A
MC3486
U107
XFR100
R105
75R
1/4W_1%
IN_AUX
VR102
78L05AC
+12VDR
C104
.01UF
63V
C105
100UF
25V
+5HDCD
R106
10K
1/4W_1%
U103
PMD-100
DEMP
HDCD
ERRFLAG
GCD5ANAR
SHEET 5
DDR
BCK
LRCK
GCD5ANAL
SHEET 4
DDL
BCK
LRCK
+5VDD
C108
35V
R107
10K
1/4W_1%
U101
24AA01
U102
16C65-XT/P
R111
10K
1/4W_1%
R112
10K
1/4W_1%
VOL_DOWN
VOL_UP
REM_CON
ON/OFF
GCD5CPU SHEET 2
C107
100UF
25V
D100
1N4148
R108
10K
1/4W_1%
C108
1UF
35V
C109
.1UF
35V
R113
10K
1/4W_1%
C110
18pf
100V
C111
18pf
100V
XTL100
4.19M
R109
10K
1/4W_1%
(HDCD LED ON RD0=H)
HDCD
IN_AUX
PHASE
(PLL LED ON RD2=HI
ERRFLAG
LEDDATA
LEDCLKA
LEDDAT
LEDCLK
LEDCLR
SWSCAN0
SWSCAN1
SWSCAN2
REM
SWIN1
SWIN2
SWIN3
GCD5CPU SHEET 2
TO PIA (DISPLAY BOARD)
C112
.01UF
63V
ADCOM GCD-750
SIGNAL PROCESSOR
CALIFORNIA AUDIO LABS
1751 Langley Ave.
Irvine, California 92714
Tel(714)833-3040 Fax 833-1329
CONTRACT NO.
APPROVALS
DATE
DRAWN dan d.
CHECKED
REVISED 11/26/96 RD
REVISED 02/04/97 RD
REVISED 04/22/97 RD
SIZE D
CODE IDENT NO.
DRAWING NO. GCD5SSP.SCH
SCALE
SHEET 1 OF