



Bottom Side & PCB Stackup

DRILL CHART				
SYM	DIAM	TOL	QTY	NOTE
x	0.025	+/-0.002	15	
⊠	0.030	+/-0.002	4	
⊞	0.035	+/-0.002	6	
+	0.040	+/-0.002	12	
⊕	0.045	+/-0.002	6	
◇	0.116	+/-0.002	4	NON-PLATED
○	0.125	+/-0.002	2	
TOTAL			49	

## FAB NOTES\_2-LAYER, 2-SIDED PTH, 0.062IN, HASL, LPI

1. USE FURNISHED FILM SET
2. MATERIAL: FR-4, GFN (Tg 130) EPOXY GLASS, 1-2 oz. COPPER, FINISHED THICKNESS 0.062"
3. FINISHED PCB SHALL MEET OR EXCEED UL 94V-1 RATING
4. FINISHED PCB SHALL CONFORM TO THE LATEST IPC-6011 & IPC-6012 CLASS 2
5. PLACE VENDOR & UL LOGO ON PCB. LOGO TO INDICATE A WEEK & YEAR DATE CODE. LOCATION ON SOLDER-SIDE
6. PCB FINISH SHALL BE SMOBC/HASL
7. APPLY SOLDERMASK TO BOTH SIDES OF PCB, OVER BARE COPPER PER IPC-SM-840, TYPE B, CLASS 2
8. APPLY SILKSCREEN TO BOTH COMPONENT SIDES OF PCB USING WHITE EPOXY INK
9. BREAK ALL OUTSIDE CORNERS 0.01" MIN./0.03" MAX.
10. DIMENSIONS SHOWN IS FOR FINISHED PCB
11. HOLES SIZES ARE AFTER PLATING & FINISH
12. ALL HOLES ARE PLATED THROUGH, UNLESS OTHERWISE STATED
13. HOLE TOLERANCES ARE +/- 0.002" FOR NON-PLATED & +/- 0.003 FOR PLATED
14. LAYER TO LAYER REGISTRATION TO BE WITHIN 0.005"