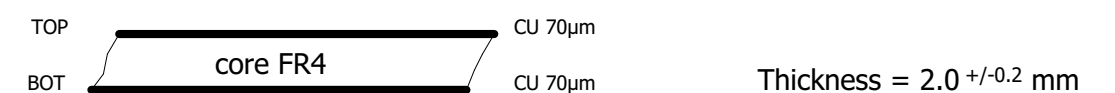


View on top side

Sym	Nº	Mils	MM	Qty	Plated
+	1	12	0.30	14377	YES
×	2	24	0.61	6	YES
▣	3	28	0.70	8	YES
◇	4	31	0.80	37	BOTH
×	5	32	0.81	54	YES
▣	6	35	0.90	12	YES
+	7	36	0.91	8	YES
+	8	39	1.00	92	YES
×	9	40	1.02	57	YES
×	10	49	1.25	2	YES
▽	11	51	1.30	20	YES
△	12	52	1.32	12	YES
△	13	55	1.40	2	YES
▽	14	59	1.50	15	YES
⊙	15	71	1.80	5	YES
●	16	87	2.20	20	YES
▣	17	100	2.54	2	YES
▣	18	126	3.20	22	NOT
+	19	173	4.40	4	NOT

Layer stackup:



REV	IND	DATE	CHANGE HISTORY	ISSUE	DIY Community	THIS DRAWING CONTAINS PROPRIETARY INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION	2017 ©	REV
				DRAWN	TITLE Printed Circuit Board			001
				JPS64 05/07/18	Nelson Pass B1-KORG Clone - THT version			
				CHECKED	DATE 05.07.2018 14:32		DRG N°	
				DATE	FILE: B1-KORG_THT_00a		PAGE:	
001		05.07.2018	First iteration					