

Diagram illustrating the cross-section of a PCB structure:

- TOP layer (CU 35µm)
- FR4 core
- BOT layer (CU 35µm)

Remaark: vias are covered

REV	IND	DATE	CHANGE HISTORY	ISSUE	DIY Community	THIS DRAWING CONTAINS PROPRIETARY INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION	2017	©		
				DRAWN	TITLE Printed Circuit Board					REV
				JPS64 08.12/17						AKSA - LENDER - PREAMPLIFIER
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