

DDDAC 1794S NOS

A Modular DAC,
based on PCM1794,
WM8804 and USB
WaveIO,

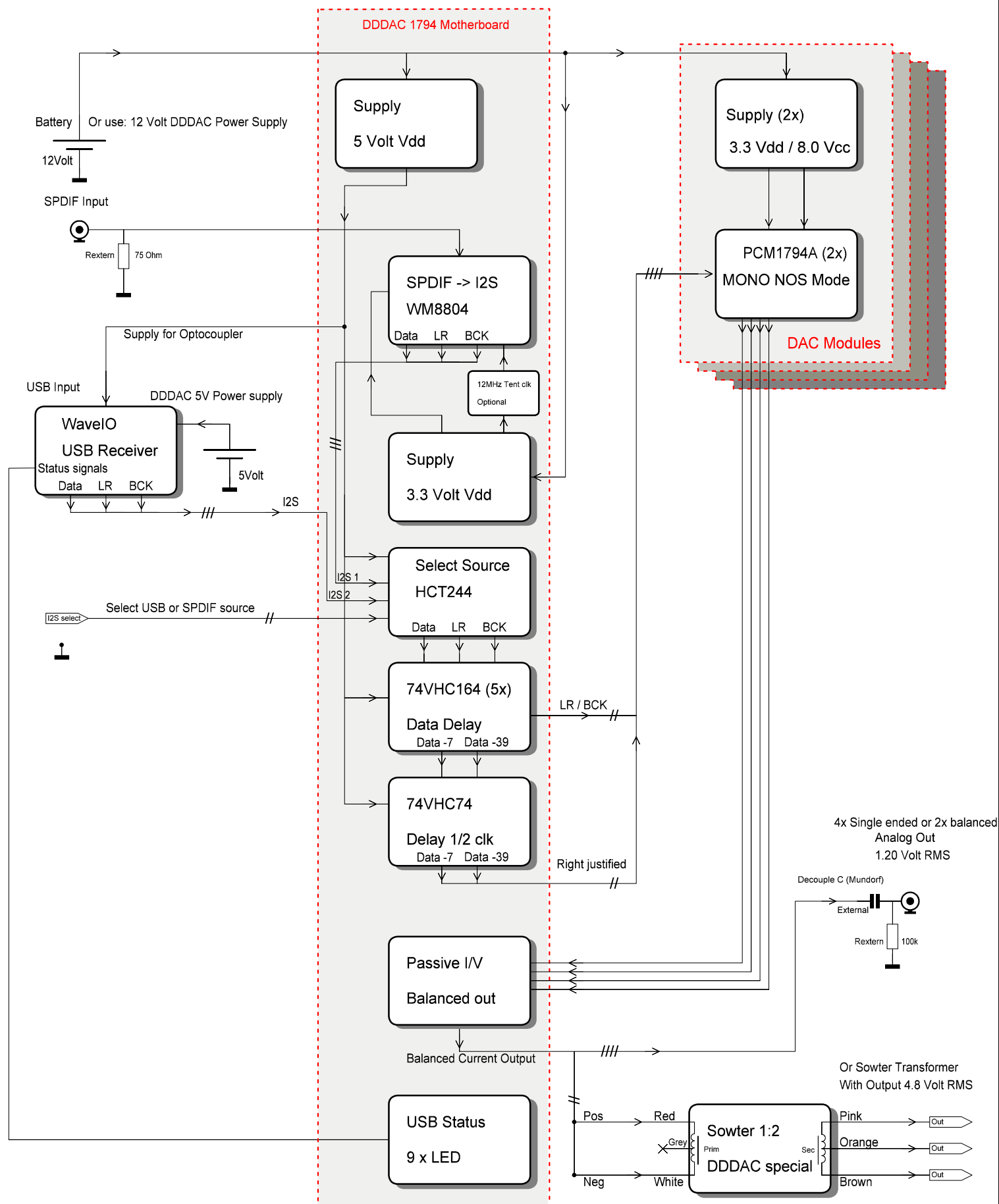
USB	384 kHz
SPDIF	192 kHz

By Doede Douma

Version 4.1

Updated January 5th, 2014

<http://www.dddac.com/>



Notes for data delay section:

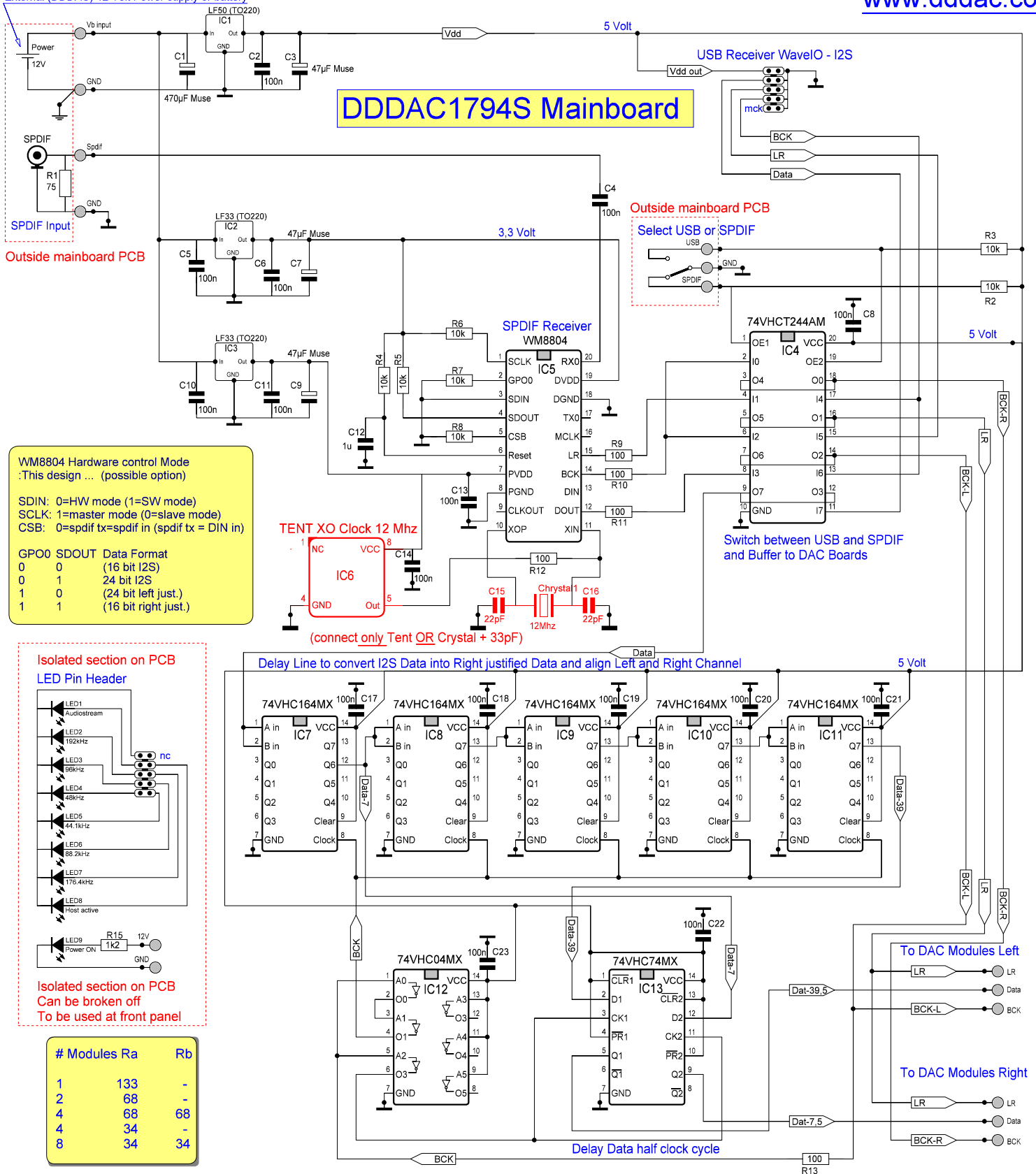
Data delay is needed to convert I2S data into right justified datastream to work with "no digital filter" mode

Datastream must be delayed by 7 clock cycles (31 - 24)

Left Channel must be delayed another 32 clock cycles to align left and right channel sample

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Update: 01-01-2014		Author: Doede Douma	
Revision: 4.0	Built: YES	Design ready: YES	Page: 1 # 9



Analog Out options / Only one Channel shown

Outside mainboard PCB

Analog Out single ended or symmetrical

Capacitor coupled

Outside mainboard PCB

Transformer coupled

Notes:

Rload depends on number of DAC Modules being used (so far up to 8 boards tested)

Rload = 134 / number of DAC Modules

Rload can be combined on PCB by paralleling 2 resistors - see table at left

Output Capacitor should always be changed to one of own personal choice ! This is the best tweak possible

*) Optional HF filter Capacitor. Best use MKP or similar. Value not critical. Approx 4.7nF to 10nF per Deck

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Built: YES

Design ready: YES

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