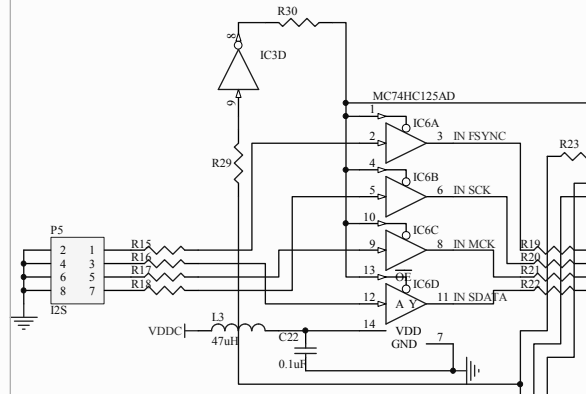
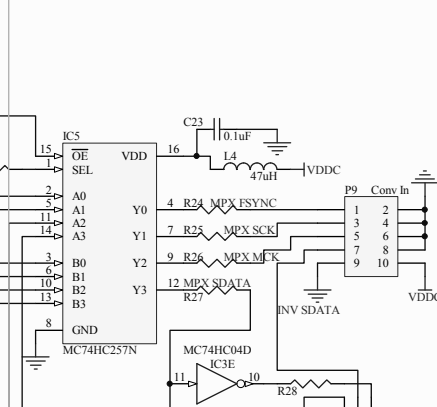


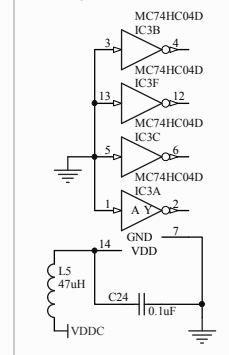
# I2S input + buffer



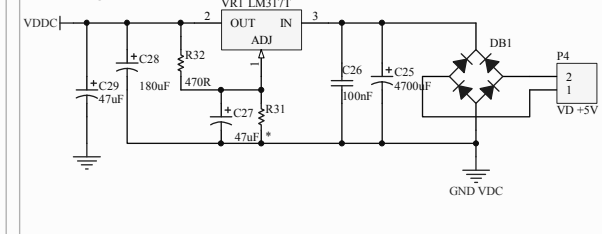
# SPDIF / I2S selector



# Hidden digital pins

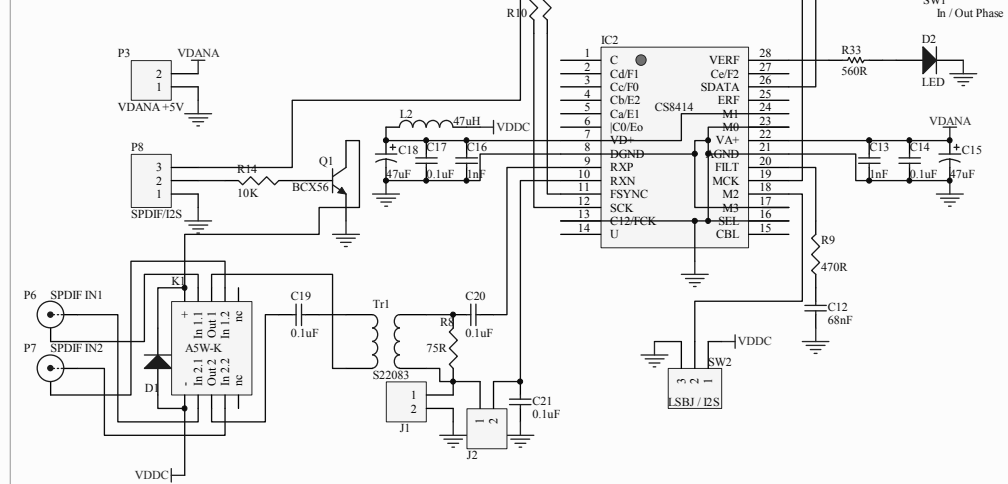


# Common digital PSU

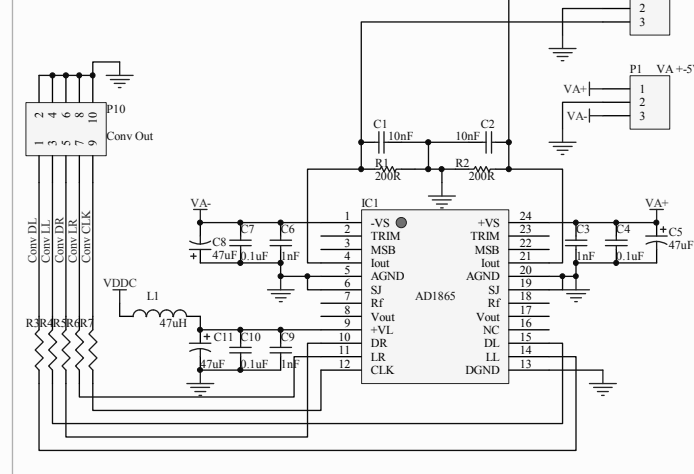


# SPDIF Receiver

## Selector I2S / SPDIF1 / SPDIF2



# AD converter



Title		
Size	Number	Revision
A3		
Date:	2010.07.30.	Sheet of
File:	F:\Hobby\ShifterDAC\SchDoc	Drawn By: