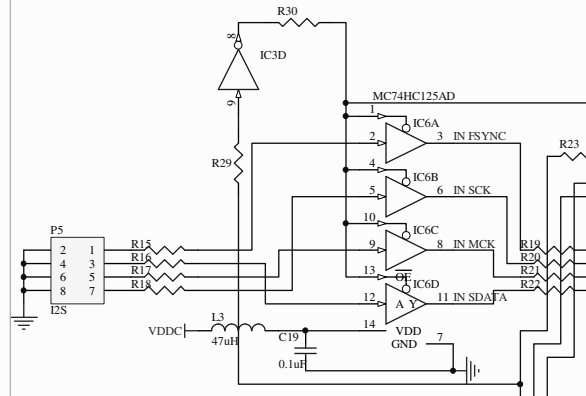
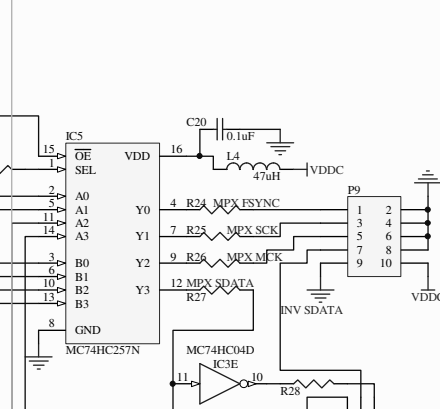


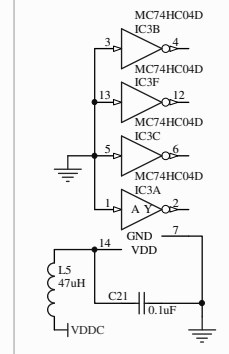
I2S input + buffer



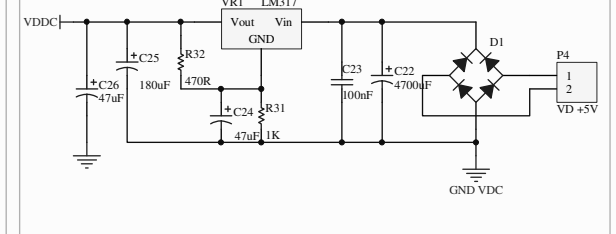
SPDIF / I2S selector



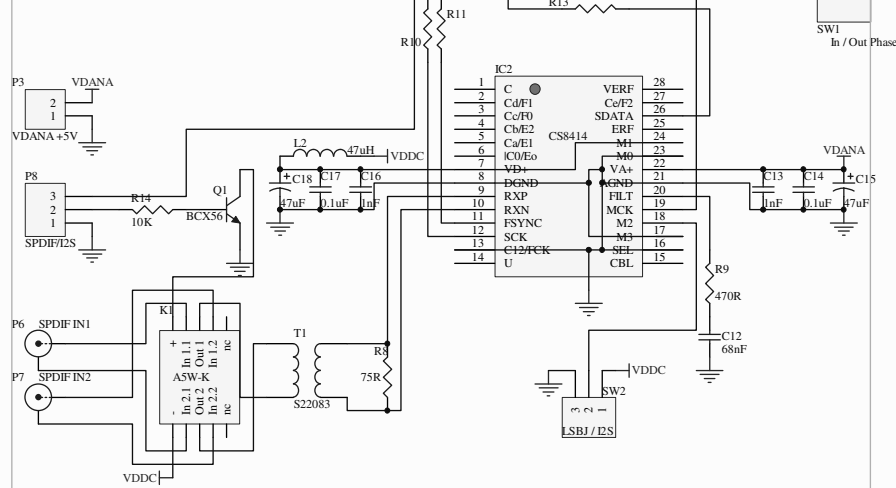
Hidden digital pins



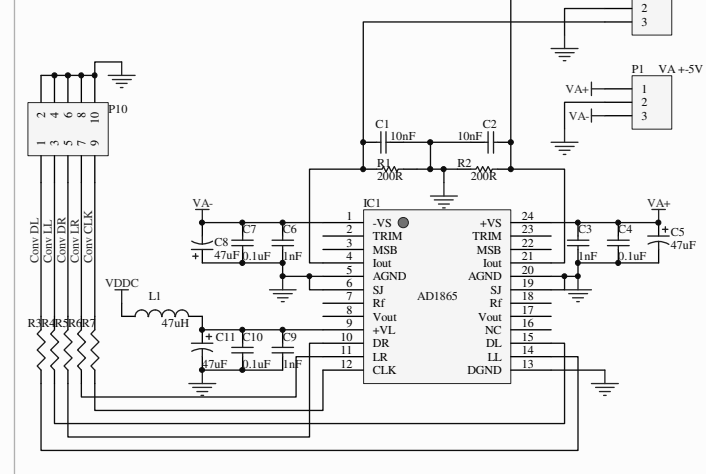
Common digital PSU



SPDIF receiver



AD converter



Title		
Size	Number	Revision
A3		
Date:	2010.07.22.	Sheet of
File:	F:\Hobby\ShifterDAC.SchDoc	Drawn By: