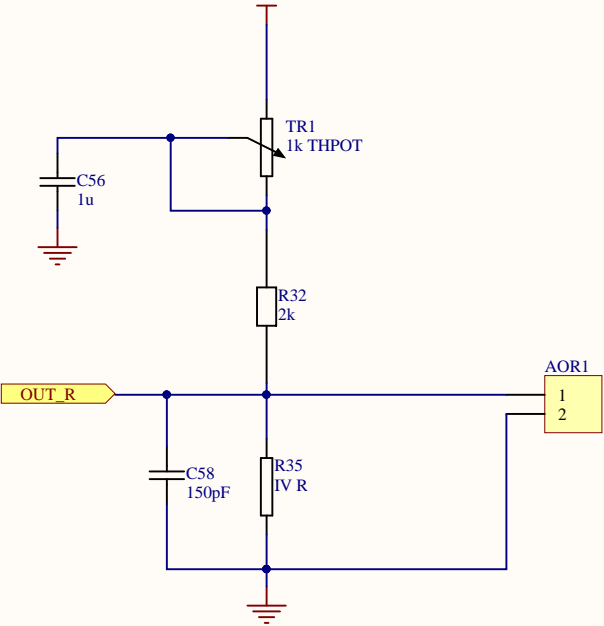
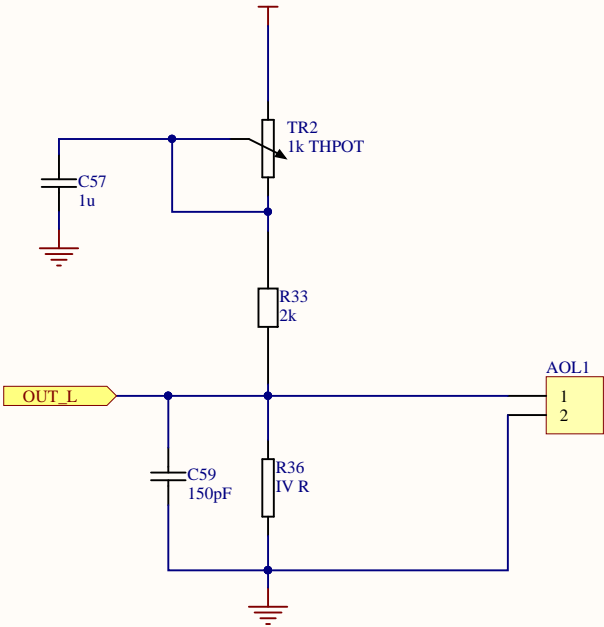


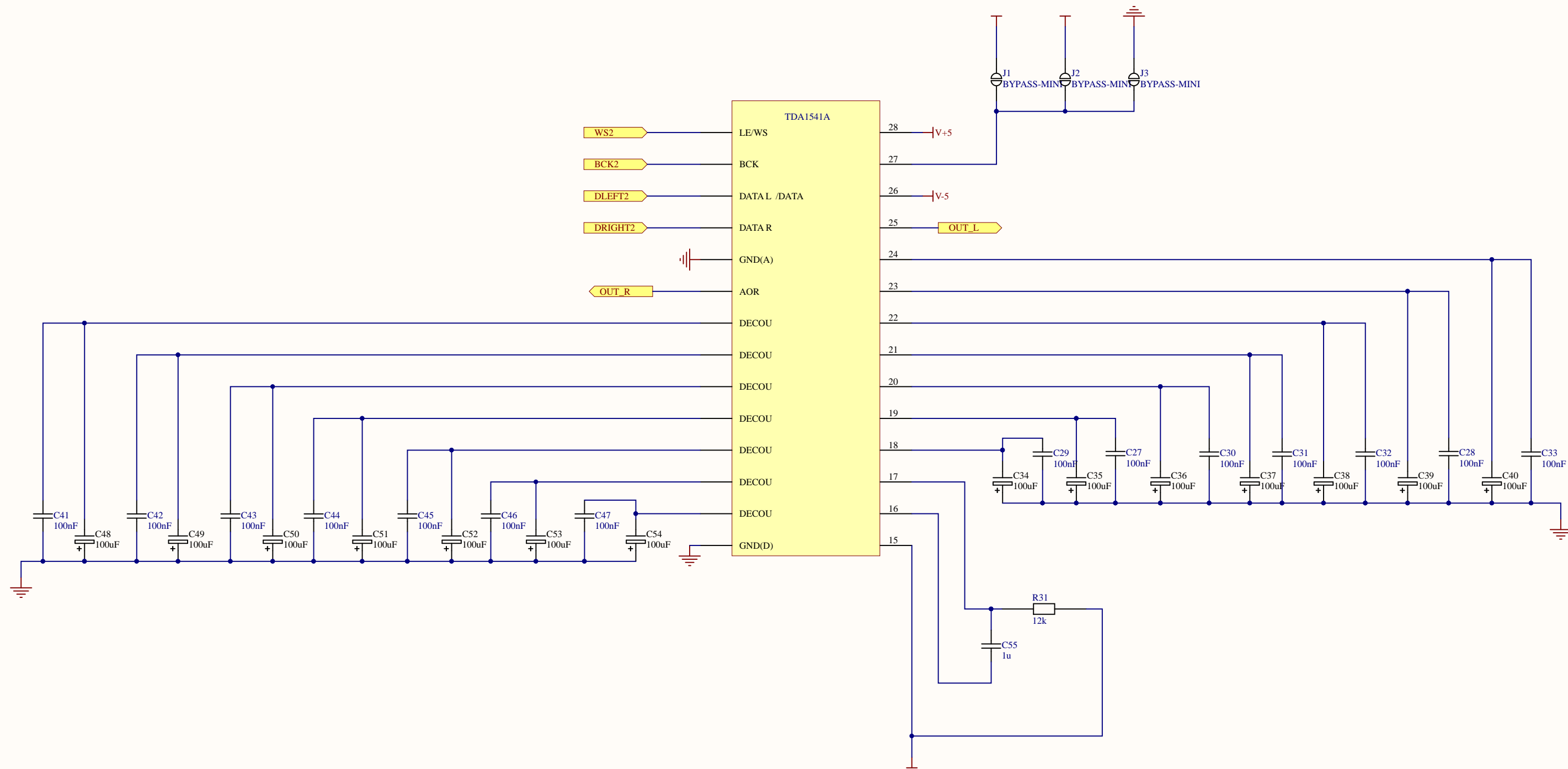
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Size A4	Number	Revision 1.0
Date:	4/07/2019	Sheet of
File:	C:\Users\...\AttenuationD3.SchDoc	Drawn By: ryanj

Note:

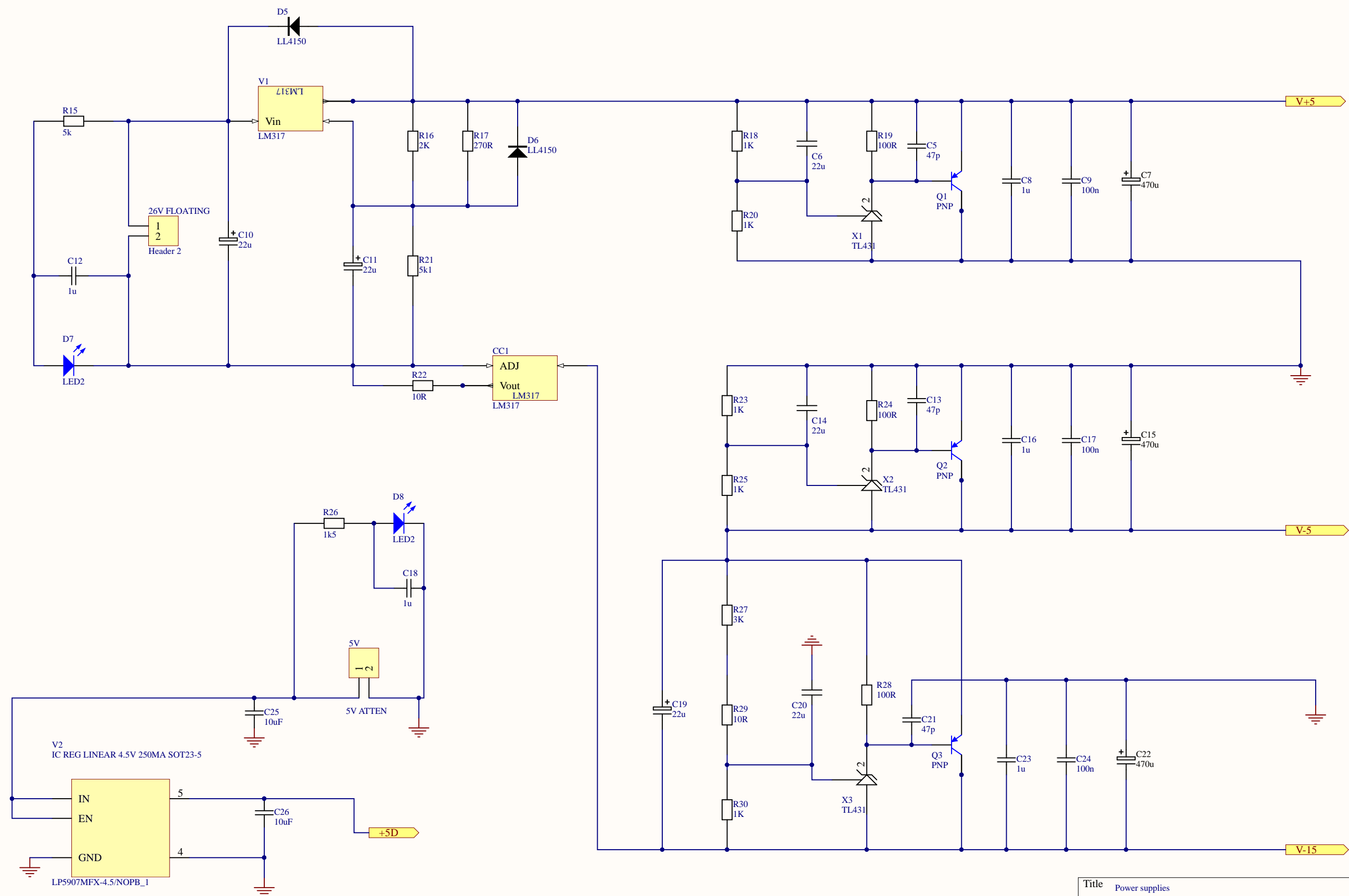
This section of the PCB forms the current bias for the current offset, and the IV conversion, dont populate if you have your own output stage.



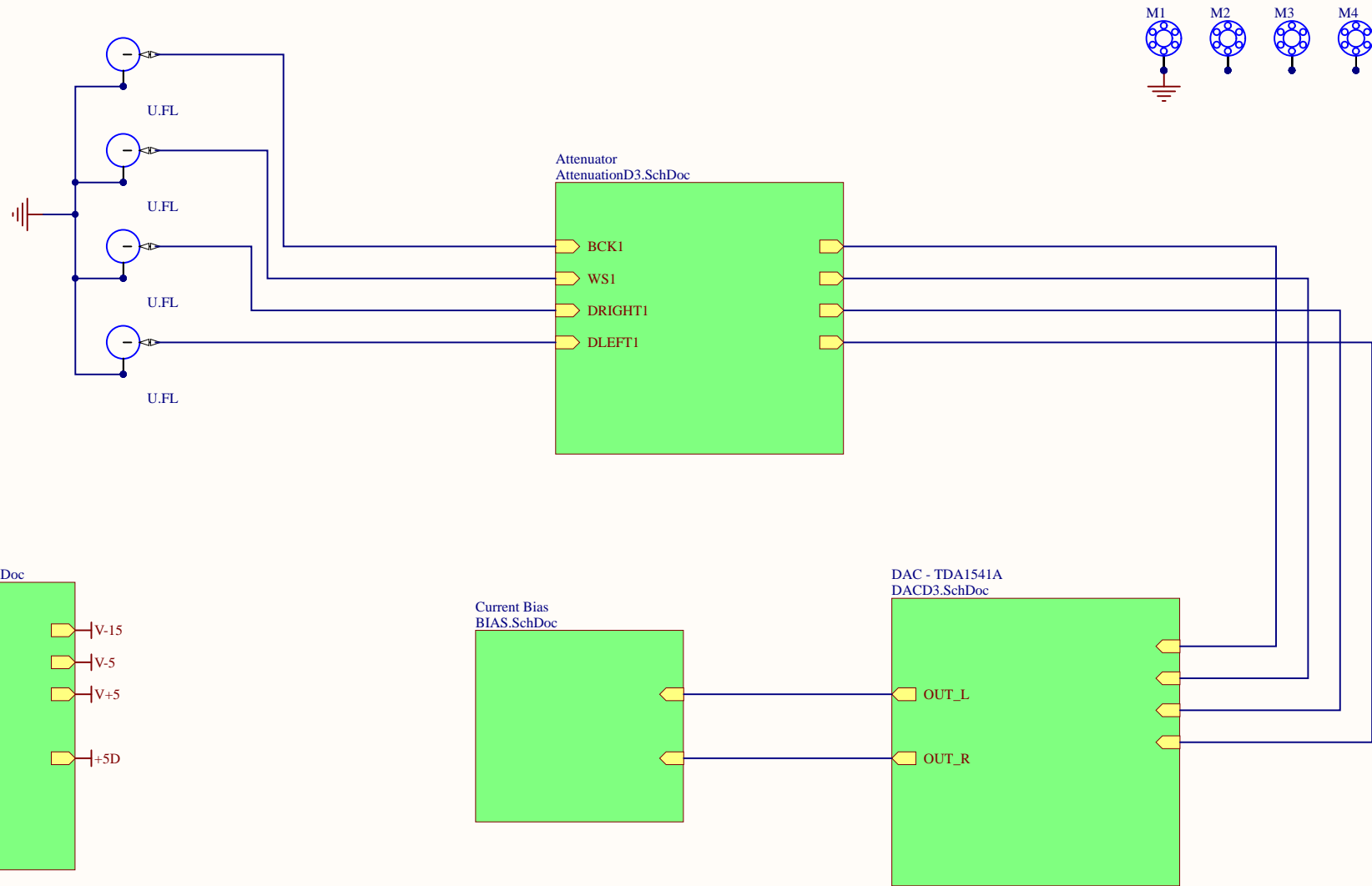
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A4		
Date:	4/07/2019	Sheet of
File:	C:\Users\...\BIAS.SchDoc	Drawn By: ryanj



Title			DAC
Size	Number	Revision	
A3		1.0	
Date:	4/07/2019	Sheet of	
File:	C:\Users\...\DACD3.SchDoc	Drawn By:	ryanj



Title Power supplies		
Size A3	Number	Revision 1.0
Date: 4/07/2019	Sheet of	
File: C:\Users\...\PSUD3.SchDoc	Drawn By: ryanj	



Title		
Net Connections		
Size	Number	Revision
A4		1.0
Date:	4/07/2019	Sheet of
File:	C:\Users\...\TDA1541A.SchDoc	Drawn By: ryanj