

Pin No.	Symbol	I/O	Description	
29	DB08	I/O	Data 8 (MSB)	Data Bus line for the EXT. RAM (CXK5816P-15L)
30	DB07	I/O	Data 7	
31	DB06	I/O	Data 6	
32	DB05	I/O	Data 5	
33	VDD	—	+5V	
34	DB04	I/O	Data 4	Data Bus line for the EXT. RAM (CXK5816P-15L)
35	DB03	I/O	Data 3	
36	DB02	I/O	Data 2	
37	DB01	I/O	Data 1 (LSB)	
38	RA01	O	ADDR01 (LSB)	Address signal output for the EXT. RAM (CXK5816P-15L)
39	RA02	O	ADDR02	
40	RA03	O	ADDR03	
41	RA04	O	ADDR04	
42	RA05	O	ADDR05	
43	RA06	O	ADDR06	
44	RA07	O	ADDR07	
45	RA08	O	ADDR08	
46	RA09	O	ADDR09	
47	RA10	O	ADDR10	
48	RA11	O	ADDR11 (MSB)	
49	RAWE	O	Write enable signal output “L” active	
50	RACS	O	Chip select signal output “L” active	
51	C4M	O	Not used	
52	VSS	—	GND (0V)	
53	XTAI	I	X'TAL OSC. input f= 8.4672 MHz	
54	XTAO	O	X'TAL OSC. output f= 8.4672 MHz	
55	C2FL	O	Monitor of error detection *	
56	C2PO	O	Not used	
57	RAOV	O	Not used	
58	SLOB	I	0V (GND)	
59	PSSL	I	0V (GND)	
60	APTR	O	Not used	
61	APTL	O	Not used	
62	DA01	O	C1F1 *	
63	DA02	O	C1F1 *	
64	DA03	O	C2F1 *	Not used
65	DA04	O	C2F2 *	Not used
66	DA05	O	UGFS	Not used
67	DA06	O	W $\overline{\text{FCK}}$ INV write frame clock signal output	
68	DA07	O	Not used FCKV 1/4 or 1/8 W $\overline{\text{FCK}}$ output	
69	DA08	O	Not used FCKX 1/4 or 1/8 R $\overline{\text{FCK}}$ output	
70	DA09	O	Not used $\overline{\text{PLCK}}$ 1/2 VCO output	
71	DA10	O	Not used $\overline{\text{LRCK}}$ INV. LRCK (Pin 80)	
72	DA11	O	Not used C4LR 176.4 kHz strobe signal output	
73	VDD	—	+5V	
74	DA12	O	Not used $\overline{\text{DENL}}$ L-ch serial data enable signal	
75	DA13	O	Not used $\overline{\text{DENR}}$ R-ch serial data enable signal	
76	DA14	O	C210 INV. C210 (Pin 77)	
77	DA15	O	Not used C210 Bit clock	f= 2.1168 MHz
78	DA16	O	Data output	
79	WDCK	O	Word clock output	88.2 kHz strobe signal
80	LRCK	O	L-ch, R-ch clock output	44.1 kHz strobe signal

* Monitor Signal refer to Fig. 1 & Fig. 2

C1F1	C1F2	C1 Correction
O	O	No error
I	O	Signal error correction
O	I	Double error correction
I	I	Irretrievable error

Fig. 1

C2F1	C2F2	C2FL	C2 Correction
O	O	O	No error
I	O	O	Signal error correction
O	I	O	Double error correction
I	I	I	Irretrievable error

Fig. 2