

Fig. 4. Residual currents in the output transistors are well controlled.

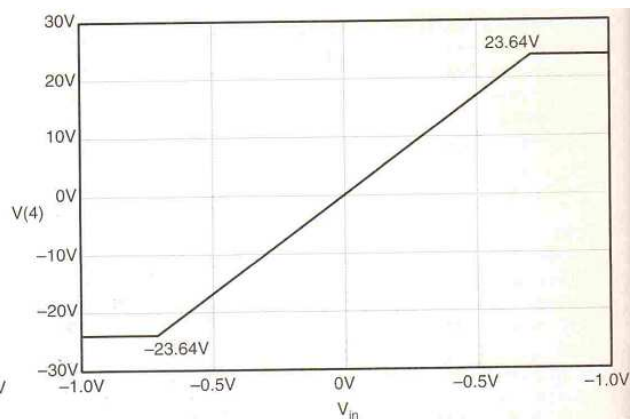


Fig. 5. Curve for output voltage swing shows that this design can drive close to the $\pm 25V$ supply rails.

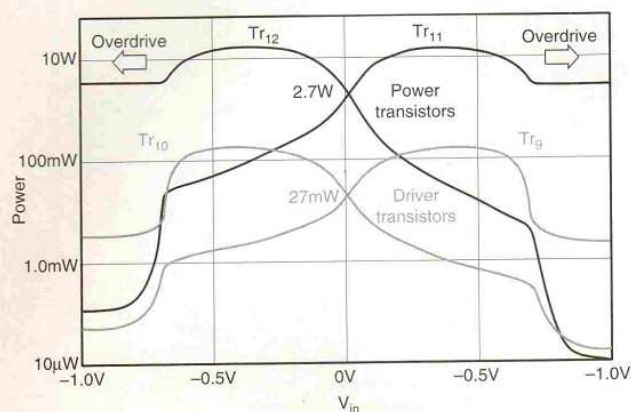


Fig. 6. Dissipation of the power transistors when overdriven illustrates the efficiency of the bias control loop.

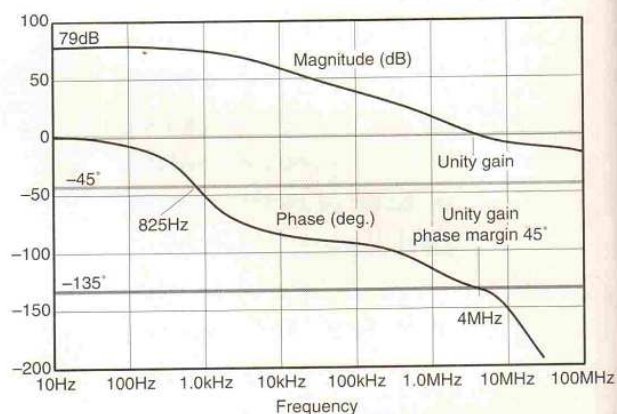


Fig. 7. Open-loop gain and phase characteristics for the amplifier.