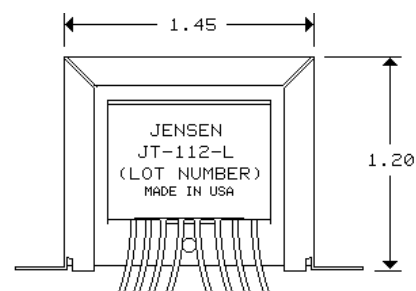


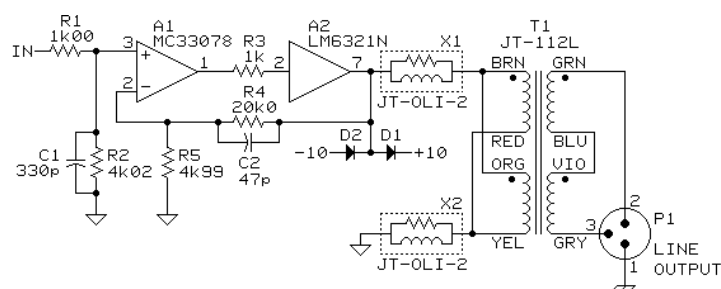
## LINE OUTPUT TRANSFORMER QUADFILAR WINDINGS

- Distortion 0.032% typ at 20 Hz and +4 dBu output level
- Wide bandwidth: -3 dB at 0.27 Hz and 370 kHz
- Drives 600  $\Omega$  loads to levels up to +21 dBu at 20 Hz
- Excellent time domain performance: DLP 0.3° typ 20 Hz to 20 kHz
- Loading loss only 1.5 dB when driving 600  $\Omega$  load

This transformer is designed for use as a 1:2 step-up for line output stages operating from low voltage rails. For 1:1 applications consider the JT-11-FLCF. The 50% nickel core alloy has excellent level handling and low distortion. Driving signals should be free of DC and source impedance as low as possible.

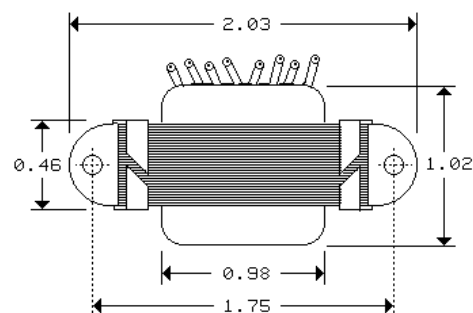


#30 AWG (7x38) UL STYLE 1061 COLOR CODED WIRE LEADS, 8" MINIMUM LENGTH

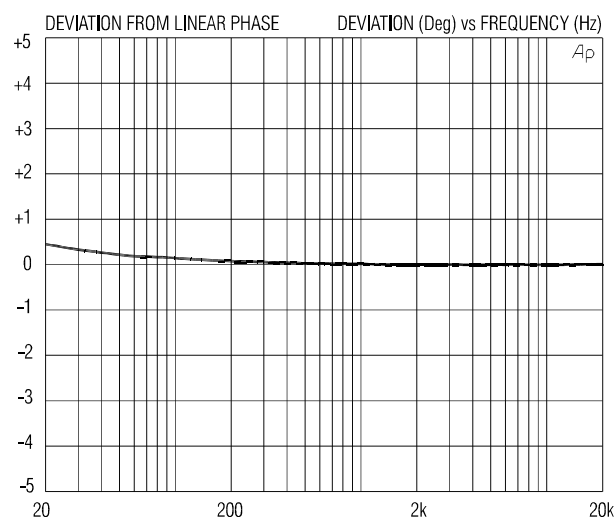
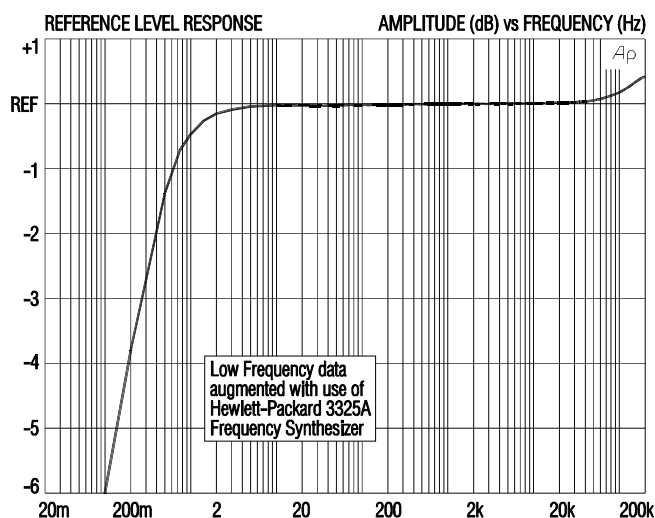


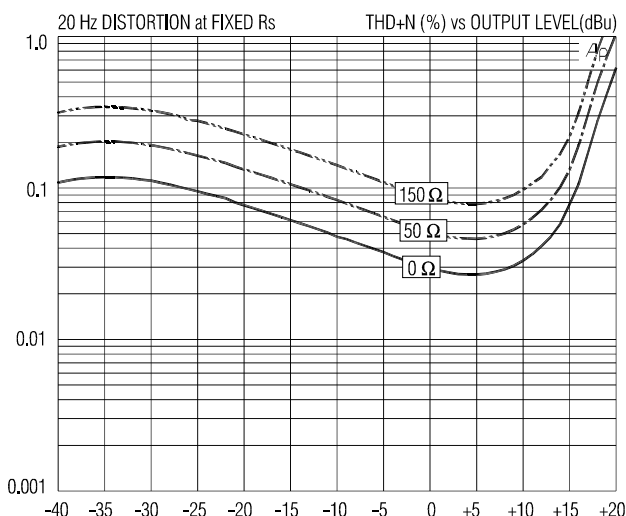
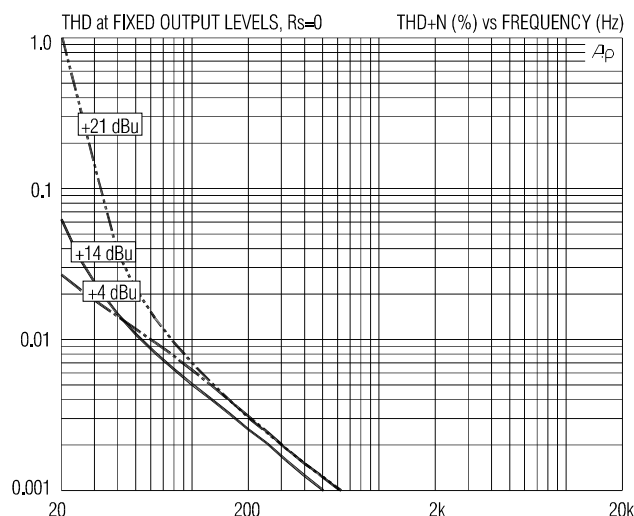
LINE DRIVER FOR HIGH OUTPUT FROM LOW SUPPLY VOLTAGES

### TYPICAL APPLICATION



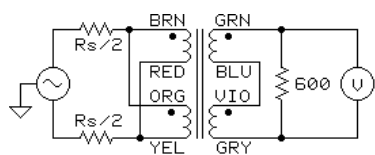
0.187 DIA MOUNTING HOLES (2 PLACES)  
FOR USE WITH #8 MOUNTING HARDWARE



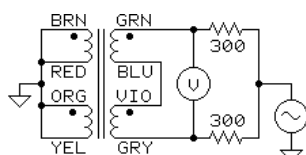


## JT-112-LCF SPECIFICATIONS (1:2 parallel/series configuration, all levels output unless noted)

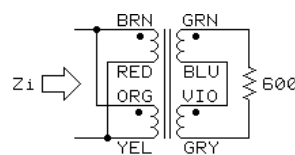
PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, $Z_i$	20 Hz to 20 kHz, 0 dBu, test circuit 3	170 $\Omega$	179 $\Omega$	190 $\Omega$
Voltage gain	1 kHz, 0 dBu, test circuit 1, $R_s=0 \Omega$	4.3 dB	4.5 dB	4.7 dB
Magnitude response, ref 1 kHz	20 Hz, 0 dBu, test circuit 1, $R_s=0 \Omega$	-0.10 dB	-0.03 dB	$\pm 0.0$ dB
	20 kHz, 0 dBu, test circuit 1, $R_s=0 \Omega$	-0.10 dB	+0.01 dB	+0.1 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, 0 dBu, test circuit 1, $R_s=0 \Omega$		+0.3/-0°	$\pm 1.0^\circ$
Bandwidth	LF -3 dB re 1 kHz, test circuit 1, $R_s=0 \Omega$		0.27 Hz	
	HF -3 dB re 1 kHz, test circuit 1, $R_s=0 \Omega$		370 kHz	
Distortion (THD)	1 kHz, +4 dBu output, test circuit 1, $R_s=0 \Omega$		<0.001%	
	20 Hz, +4 dBu output, test circuit 1, $R_s=0 \Omega$		0.032%	0.15%
Maximum output level	20 Hz, 1% THD, test circuit 1, $R_s=0 \Omega$	+19 dBu	+21 dBu	
Common-mode rejection ratio (CMRR)	60 Hz, test circuit 2		120 dB	
	3 kHz, test circuit 2	70 dB	85 dB	
Output impedance, $Z_o$	20 Hz to 20 kHz, 0 dBu, test circuit 4		116 $\Omega$	
DC resistance	each winding (secondary is 2 series connected windings)		29 $\Omega$	
Capacitance	both primaries to secondary, 1 kHz		14 nF	
	all windings to frame, 1 kHz		35 pF	
Turns ratio	paralleled primaries to secondary	1:1.999	1:2.000	1:2.001
Temperature range	operation or storage	0° C		70° C
Breakdown voltages 60 Hz, 1 minute test duration (see IMPORTANT NOTE below)	winding to winding	250 V RMS		
	winding to frame	500 V RMS		



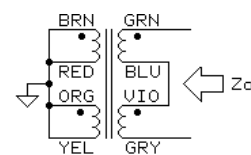
TEST CIRCUIT 1



TEST CIRCUIT 2



TEST CIRCUIT 3



TEST CIRCUIT 4

**All minimum and maximum specifications are guaranteed.** Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.

**IMPORTANT NOTE:** This device is NOT intended for use in life support systems or any application where its failure could cause injury or death. The breakdown voltage specification is intended to insure integrity of internal insulation systems; continuous operation at these voltages is NOT recommended. Consult our applications engineering department if you have special requirements.

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